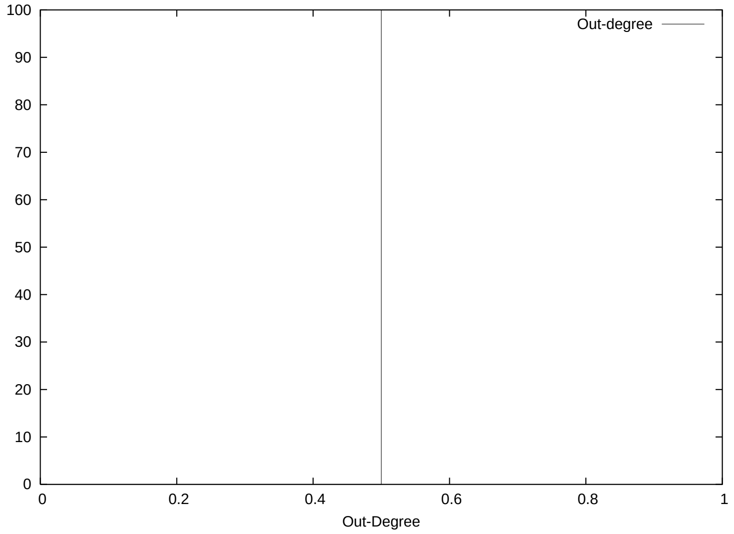
Graph: cadp/vasy_25_25

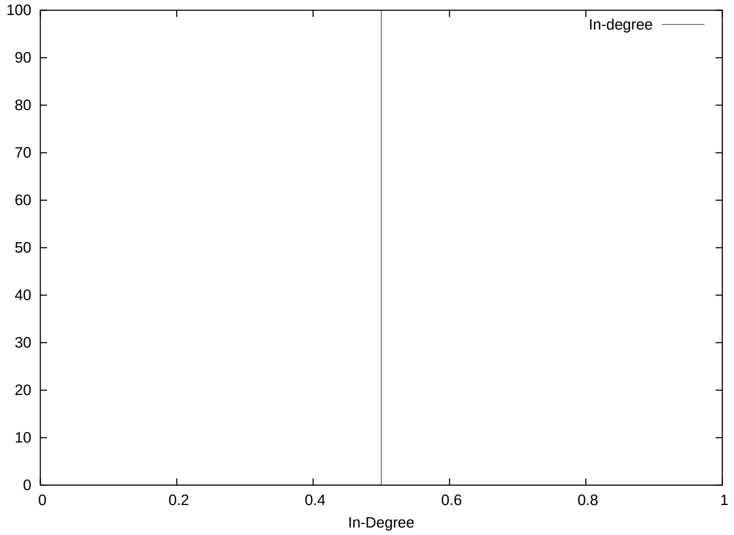
General Info

Example title	VLTS benchmark suite
Parameter	
Reduction	
Modification	
States	25,217
Edges	25,216
Average degree	0.9

Out-Degrees distribution:



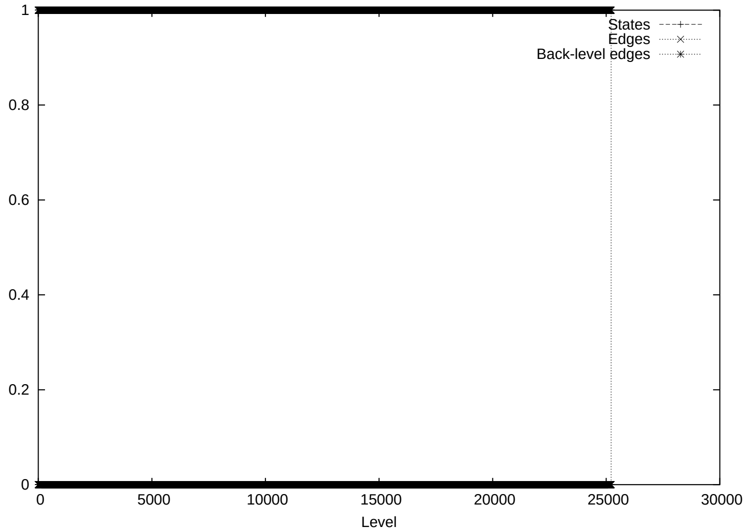
In-Degrees distribution:



BFS Info

BFS height	25,216
BFS width	1 (0%)
Back level edges	0 (0%)

BFS level info:



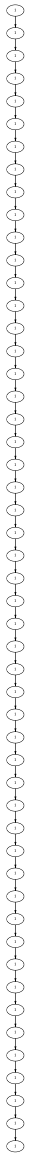
SCC Info

SCC components	25,217
SCC quetient height	25,216
SCC quetient nontrivial height	0
Number of terminal components	1
The largest component is	the only terminal

Statistics according to **sizes**:

Size	Number of components	Number of states here
1	25,217	100%

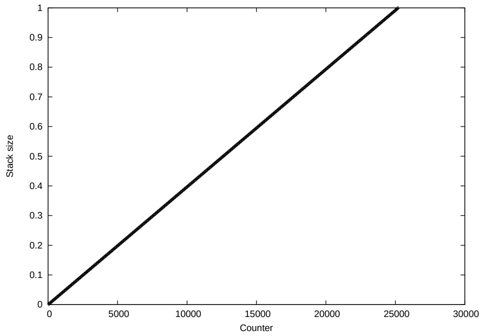
SCC component graph (part):



DFS Info

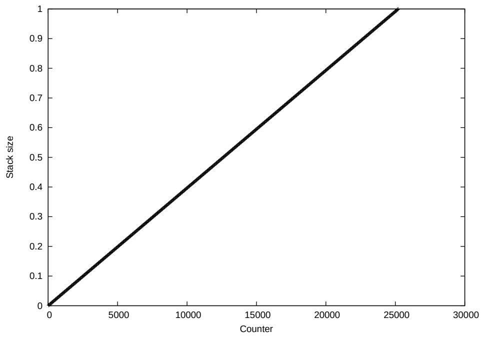
Run 0

front-edges : back-edges : cross-edges = 25,216 : 0 : 0 = 100%:0%:0%
Max. stack size: 25,217 (100%)
Shortest cycle detected: -1
Most often detected lengths of cycles: (), (), (), (), ()
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 25,216 : 0 : 0 = 100%:0%:0%
Max. stack size: 25,217 (100%)
Shortest cycle detected: -1
Most often detected lengths of cycles: (), (), (), (), ()
Stack graph:



Local Structure

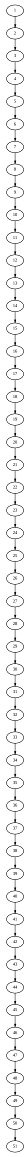
Short cycles(up to 6)
none
Diamonds

0 (0%)

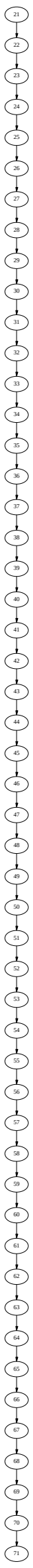
Neighborhoods

k	avg size	avg clustering
1	1.9	1
2	2.9	1
3	3.9	1
4	4.9	1

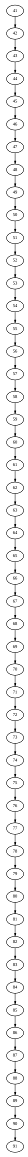
Visualization of some parts of the state state space:
Initial walk length: 0



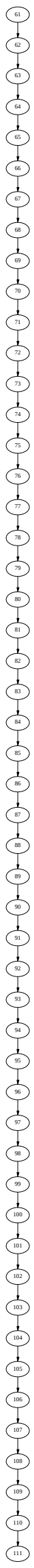
Initial walk length: 20



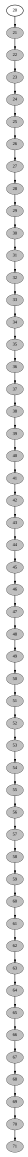
Initial walk length: 40



Initial walk length: 60



Initial walk length: 20



Initial walk length: 40

Initial walk length: 60