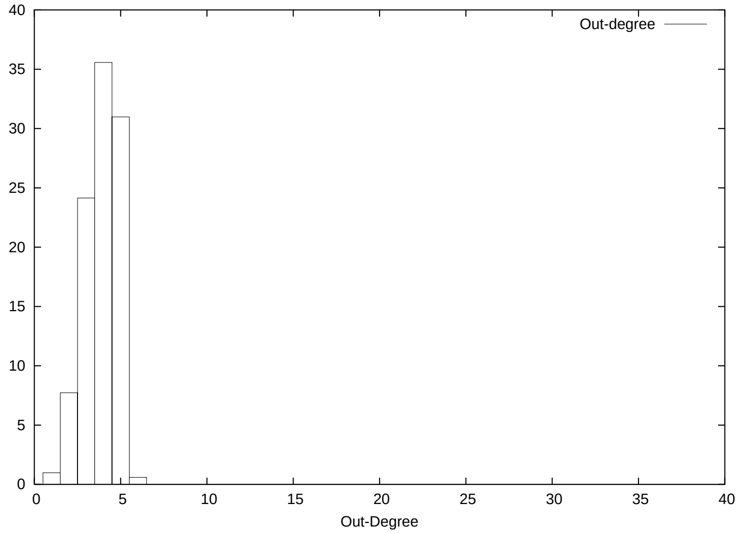
Graph: cadp/vasy_18_73

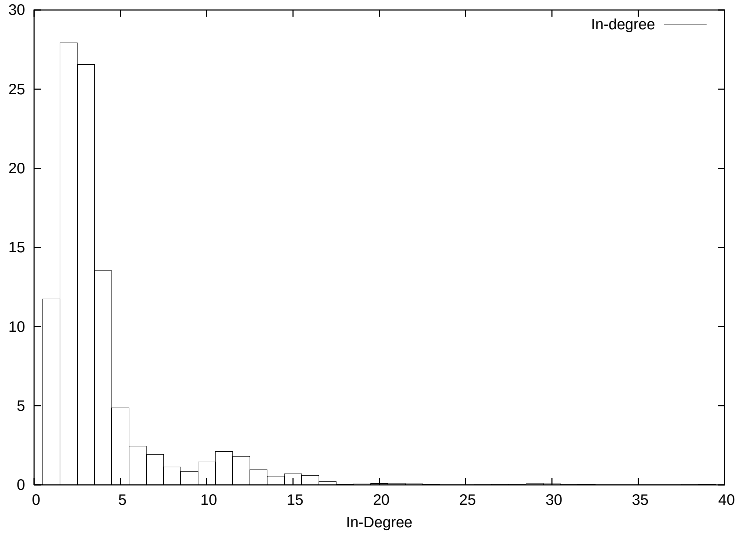
General Info

Example title	VLTS benchmark suite
Parameter	
Reduction	
Modification	
States	18,746
Edges	73,043
Average degree	3.8

Out-Degrees distribution:



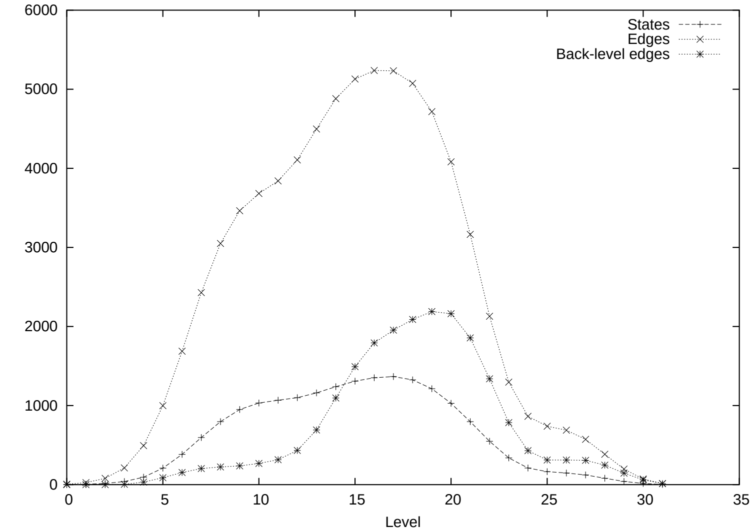
In-Degrees distribution:



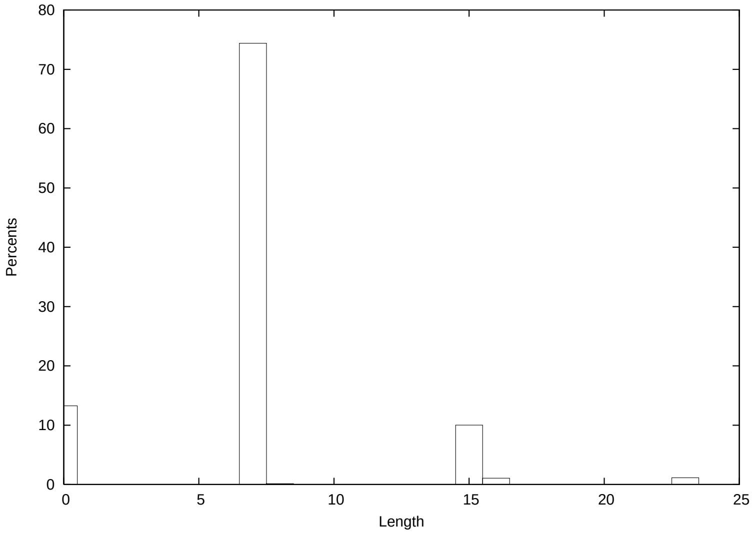
BFS Info

BFS height	31
BFS width	1,366 (7.2%)
Back level edges	21,226 (29%)

BFS level info:



Back level edges lengths:



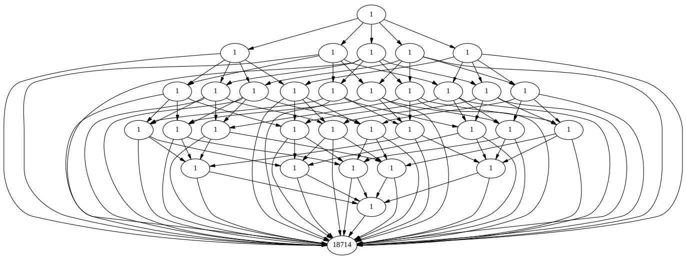
SCC Info

SCC components	33
SCC quetient height	6
SCC quetient nontrivial height	1
Number of terminal components	1
The largest component is	the only terminal

Statistics according to **sizes**:

Size	Number of components	Number of states here
18,714	1	99.8%
1	32	0.1%

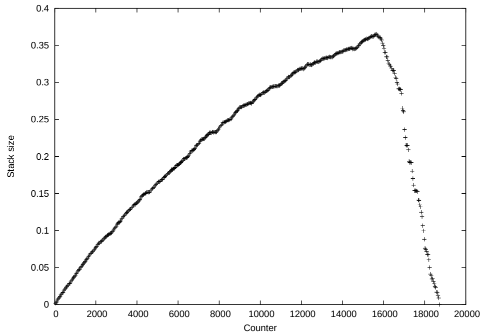
SCC component graph (part):



DFS Info

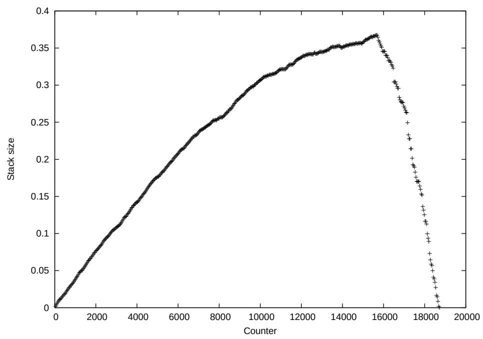
Run 0

front-edges : back-edges : cross-edges = 18,745 : 22,566 : 31,732 = 25.6%:30.8%:43.4%
Max. stack size: 6,851 (36.5%)
Shortest cycle detected: 8
Most often detected lengths of cycles: 8 (124), 17 (91), 16 (82), 25 (71), 33 (61)
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 18,745 : 23,076 : 31,222 = 25.6%:31.5%:42.7%
Max. stack size: 6,895 (36.7%)
Shortest cycle detected: 8
Most often detected lengths of cycles: 8 (131), 17 (91), 843 (64), 16 (59), 25 (55)
Stack graph:



Local Structure

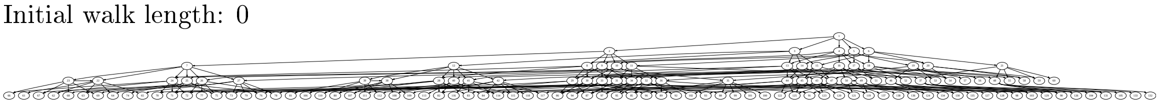
Short cycles(up to 6)
none
Diamonds

17,985 (95.9%)

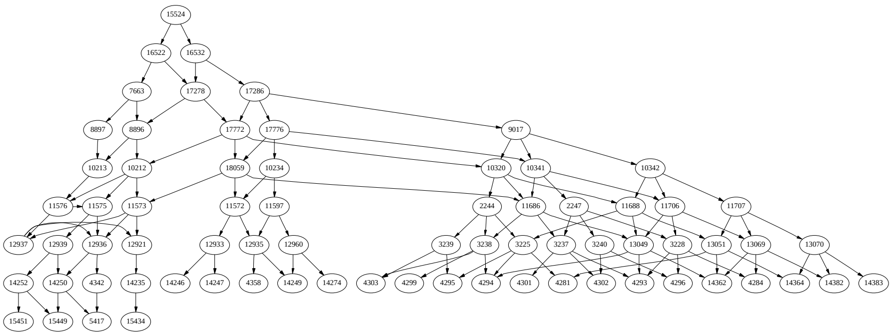
Neighborhoods

k	avg size	avg clustering
1	4.8	1
2	15.1	1.3
3	36.8	1.5
4	75.3	1.7

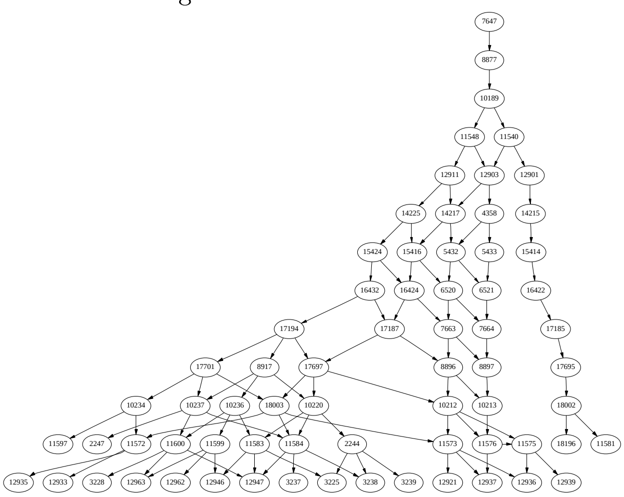
Visualization of some parts of the state state space:
Initial walk length: 0



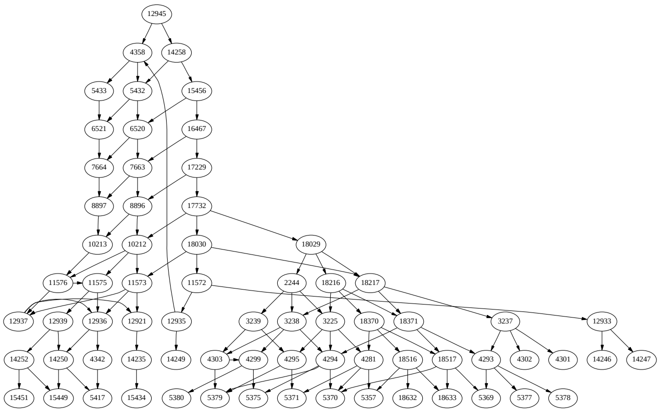
Initial walk length: 20



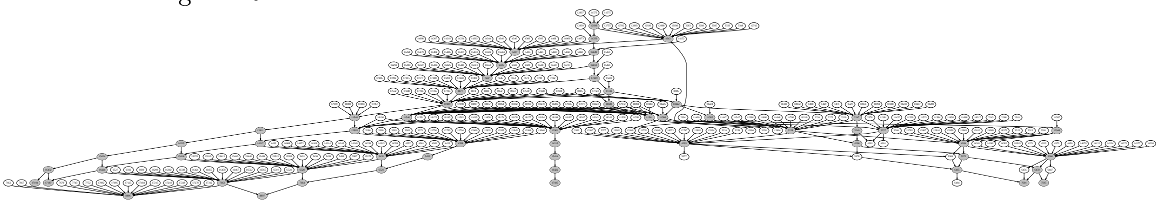
Initial walk length: 40



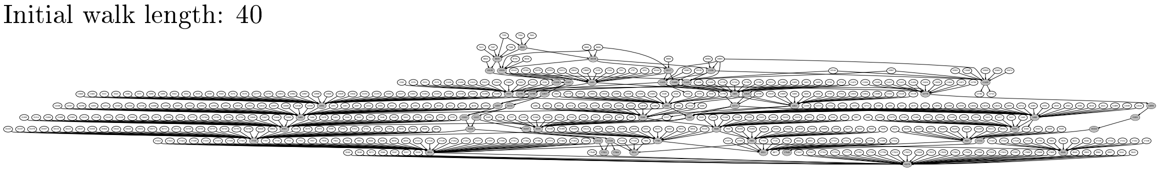
Initial walk length: 60



Initial walk length: 20



Initial walk length: 40



Initial walk length: 60