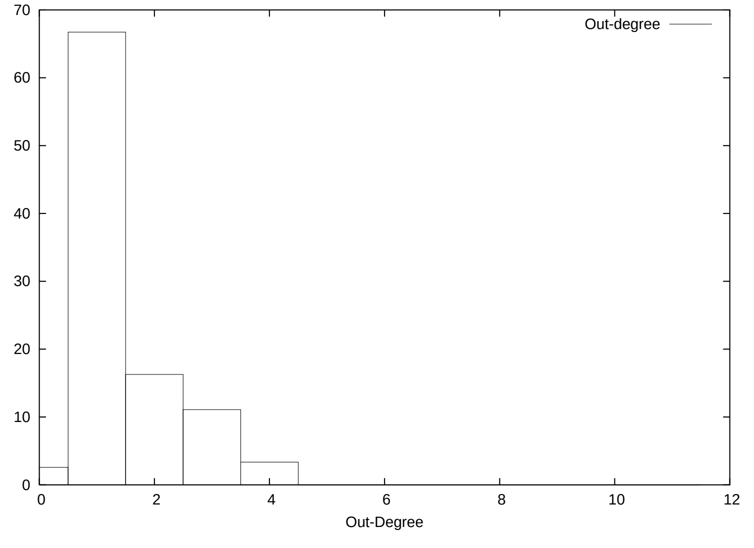
Graph: cadp/scen3_correct_2.2

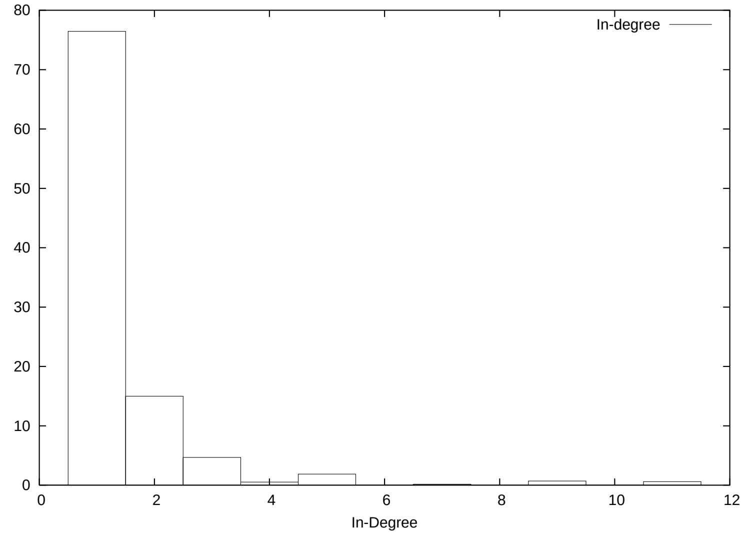
General Info

Example title	IEEE 1394 high performance serial bus
Parameter	2,2
Reduction	
Modification	version 3, correct
States	3,889
Edges	5,674
Average degree	1.4

Out-Degrees distribution:



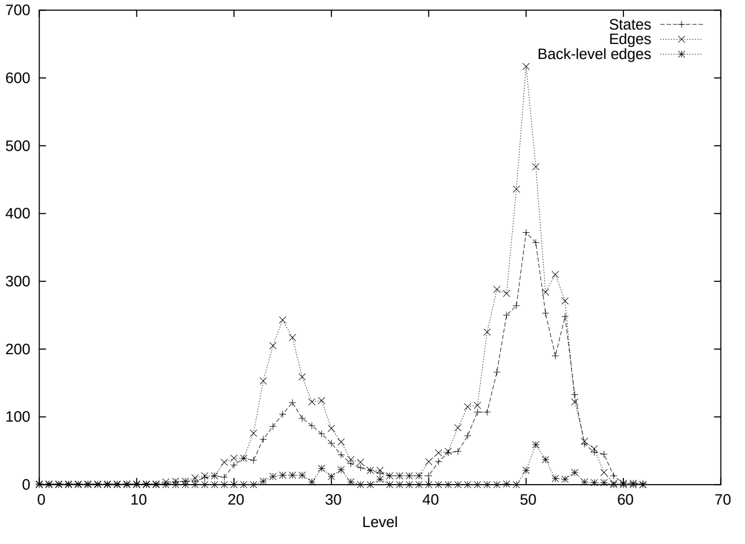
In-Degrees distribution:



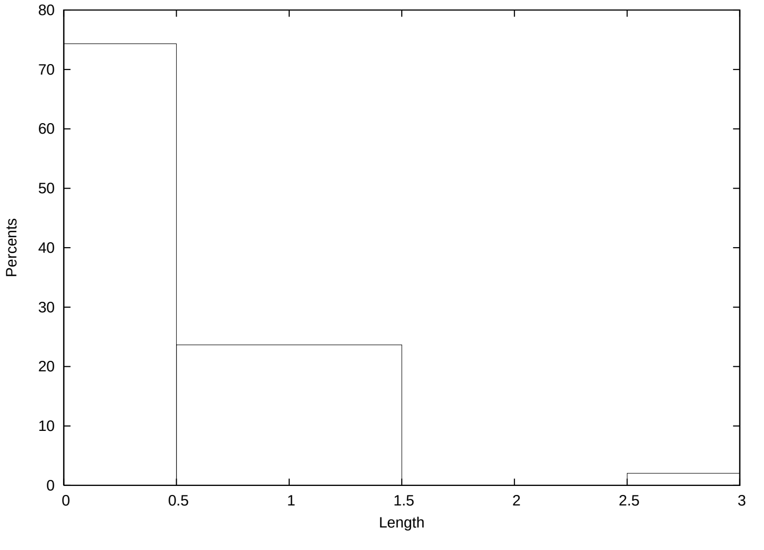
BFS Info

BFS height	62
BFS width	372 (9.5%)
Back level edges	296 (5.2%)

BFS level info:



Back level edges lengths:



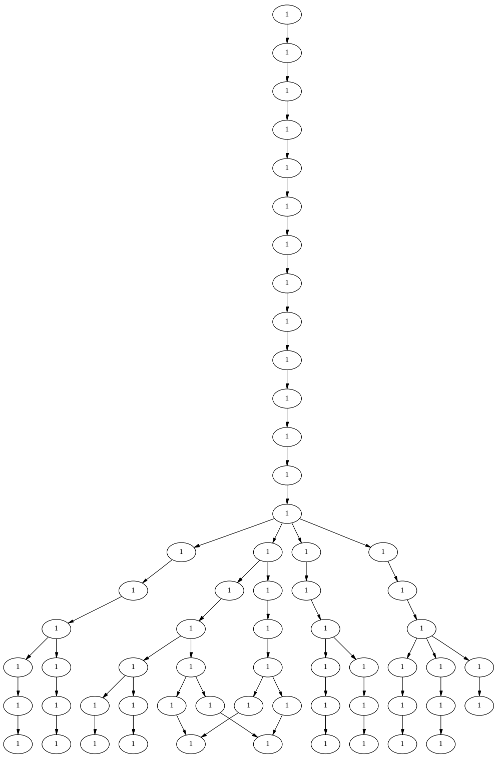
SCC Info

SCC components	3,854
SCC quetient height	66
SCC quetient nontrivial height	3
Number of terminal components	100
The largest component is	intermediate

Statistics according to **sizes**:

Size	Number of components	Number of states here
2	35	1.7%
1	3,819	98.2%

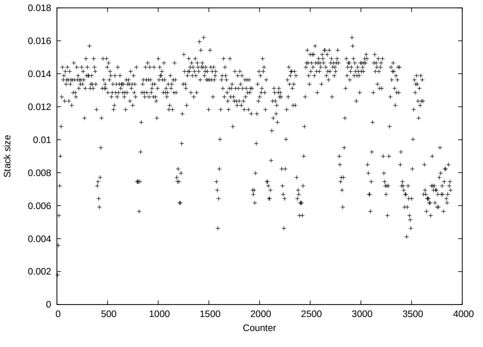
SCC component graph (part):



DFS Info

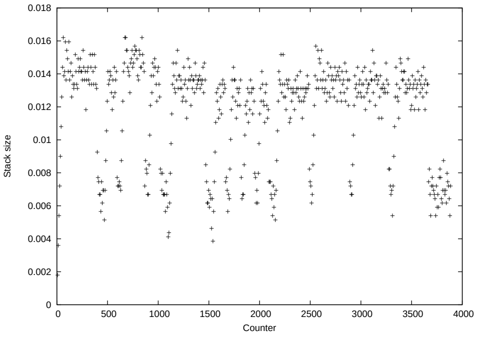
Run 0

front-edges : back-edges : cross-edges = 3,888 : 35 : 1,751 = 68.5%:0.6%:30.8%
Max. stack size: 65 (1.6%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 2 (35), (), (), (), ()
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 3,888 : 35 : 1,751 = 68.5%:0.6%:30.8%
Max. stack size: 67 (1.7%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 2 (35), (), (), (), ()
Stack graph:



Local Structure

Short cycles(up to 6)

2	70	(1.7%)

Diamonds

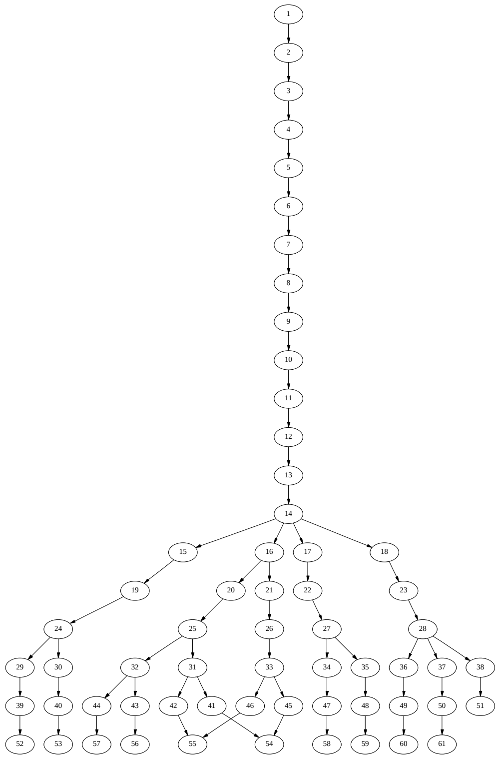
821 (21.1%)

Neighborhoods

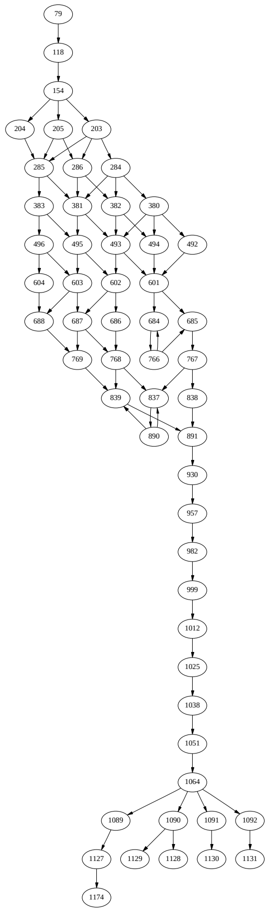
k	avg size	avg clustering
1	2.4	1
2	4.1	1
3	6.1	1.1
4	8.3	1.1

Visualization of some parts of the state state space:

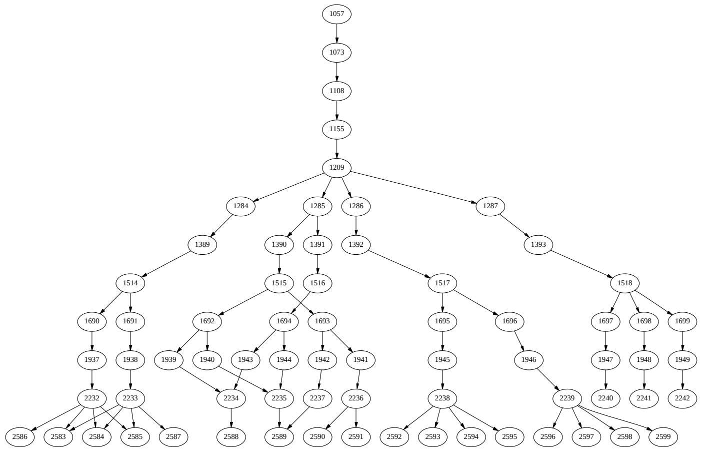
Initial walk length: 0



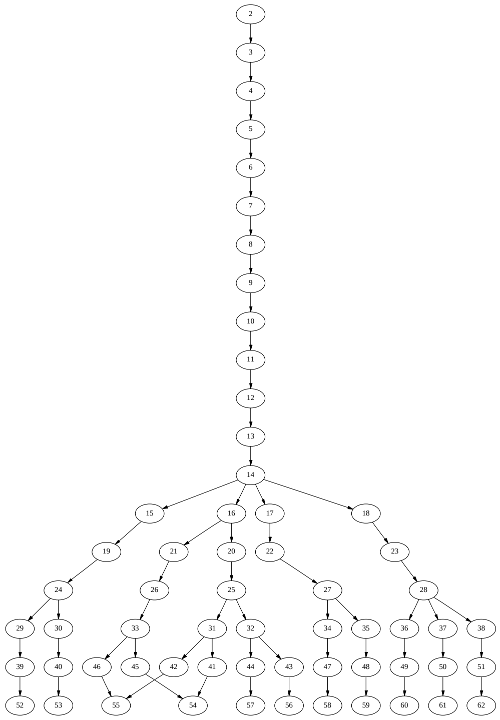
Initial walk length: 20



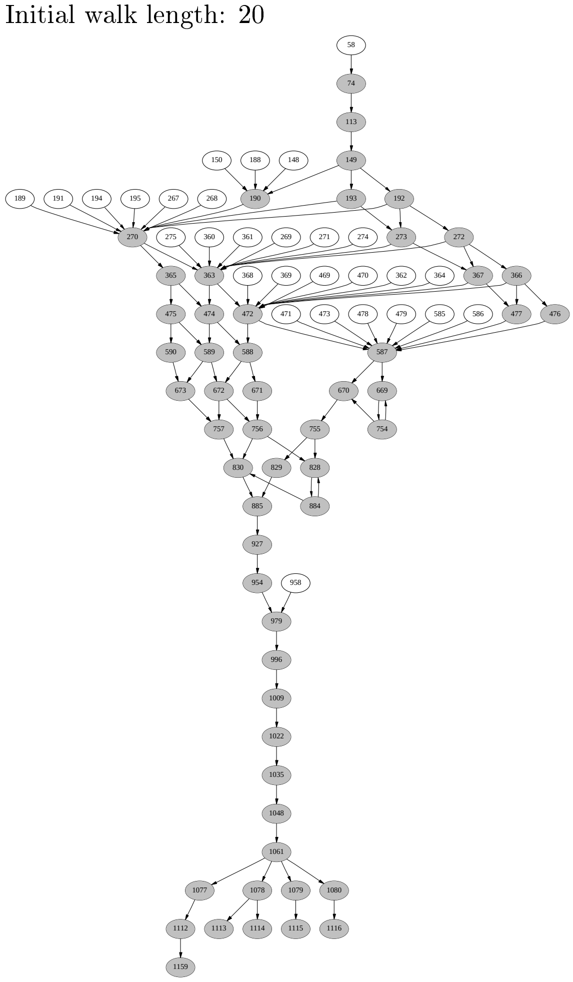
Initial walk length: 40



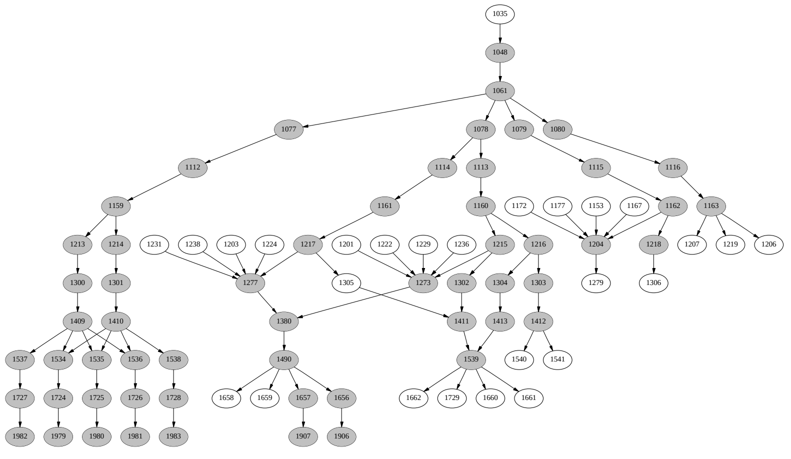
Initial walk length: 60



Initial walk length: 20



Initial walk length: 40



Initial walk length: 60