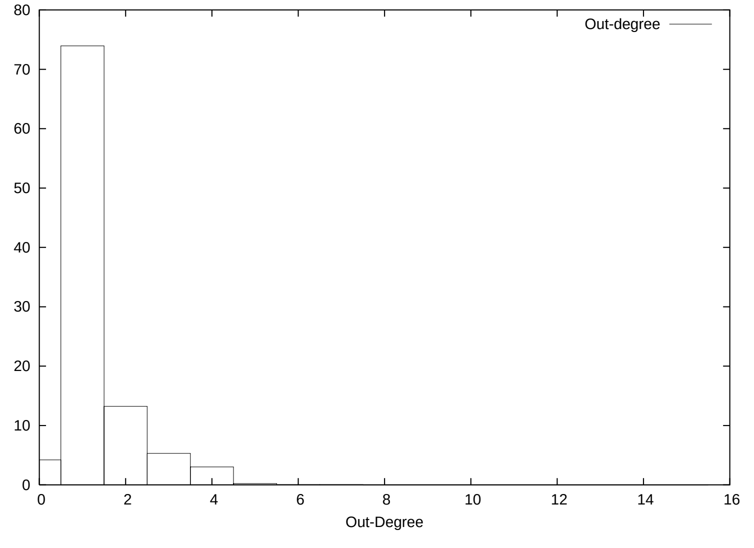
Graph: cadp/scen2_correct_2

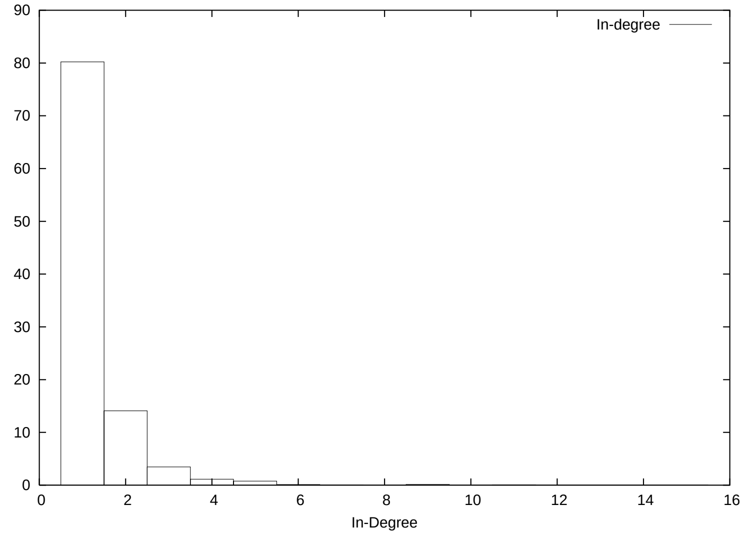
General Info

Example title	IEEE 1394 high performance serial bus
Parameter	2
Reduction	
Modification	version 2, correct
States	272,868
Edges	354,634
Average degree	1.2

Out-Degrees distribution:



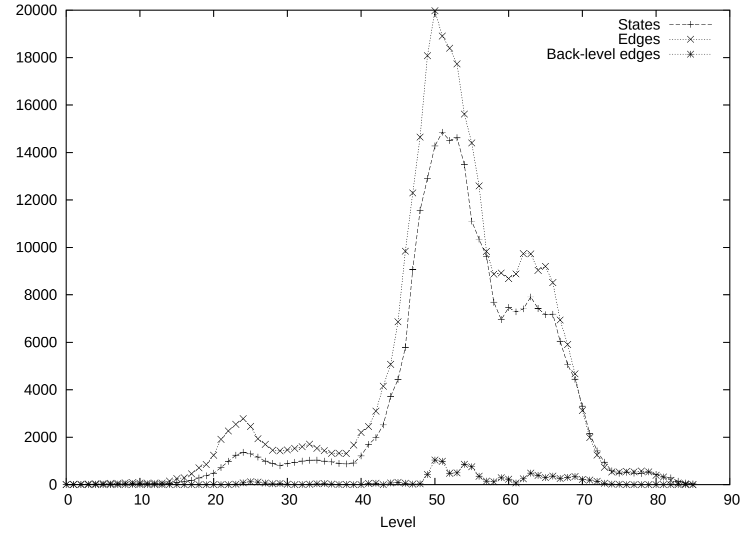
In-Degrees distribution:



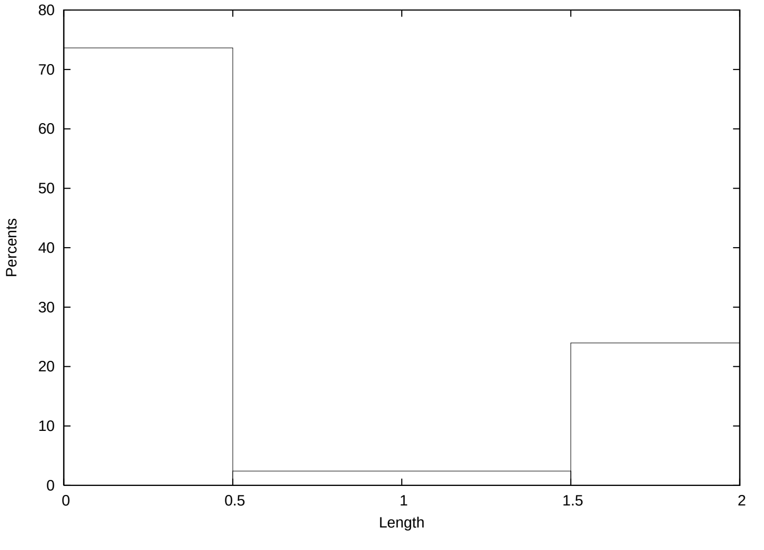
BFS Info

BFS height	85
BFS width	14,857 (5.4%)
Back level edges	10,621 (2.9%)

BFS level info:



Back level edges lengths:



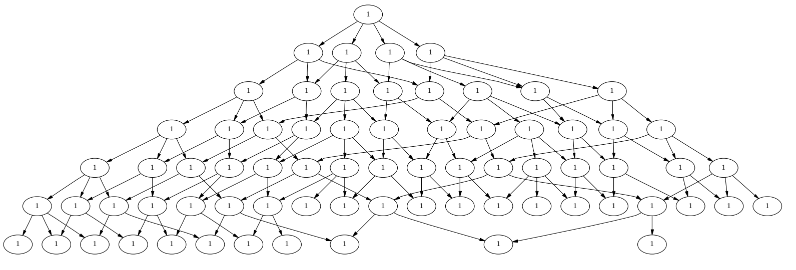
SCC Info

SCC components	271,388
SCC quetient height	93
SCC quetient nontrivial height	7
Number of terminal components	11,480
The largest component is	intermediate

Statistics according to **sizes**:

Size	Number of components	Number of states here
3	680	0.7%
2	120	0%
1	270,588	99.1%

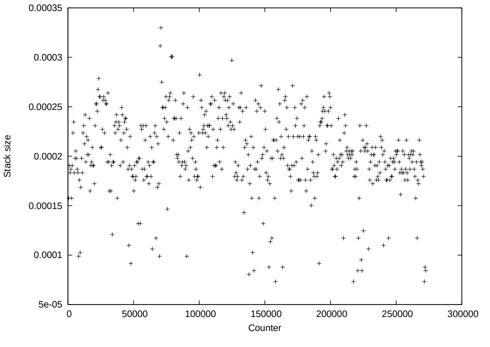
SCC component graph (part):



DFS Info

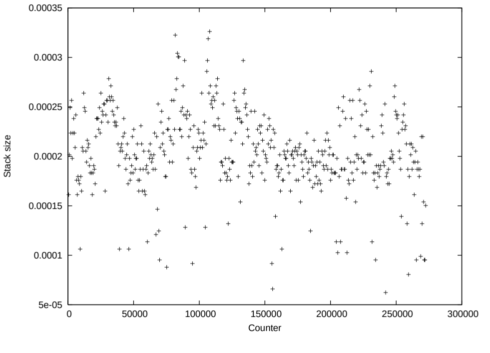
Run 0

front-edges : back-edges : cross-edges = 272,867 : 800 : 80,967 = 76.9%:0.2%:22.8%
Max. stack size: 96 (0%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 3 (680), 2 (120), (), (), ()
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 272,867 : 800 : 80,967 = 76.9%:0.2%:22.8%
Max. stack size: 95 (0%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 3 (680), 2 (120), (), (), ()
Stack graph:



Local Structure

Short cycles(up to 6)

2	240	(0%)
3	2,040	(0.7%)

Diamonds

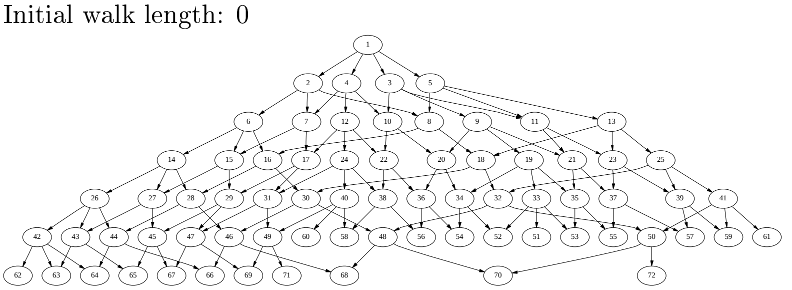
40,062 (14.6%)

Neighborhoods

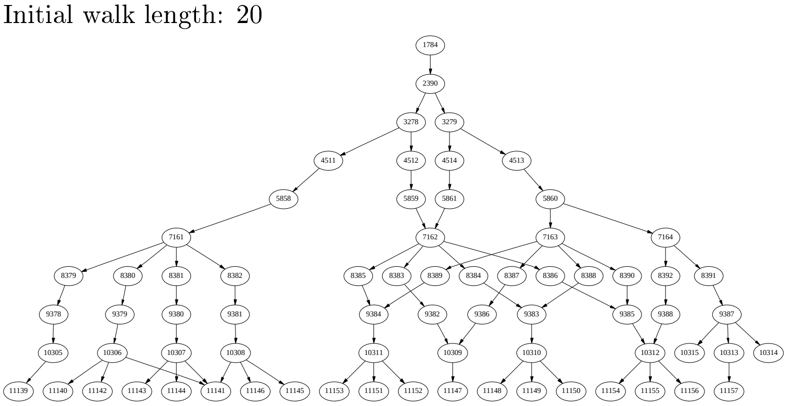
k	avg size	avg clustering
1	2.2	1
2	3.7	1
3	5.3	1
4	7	1.1

Visualization of some parts of the state state space:

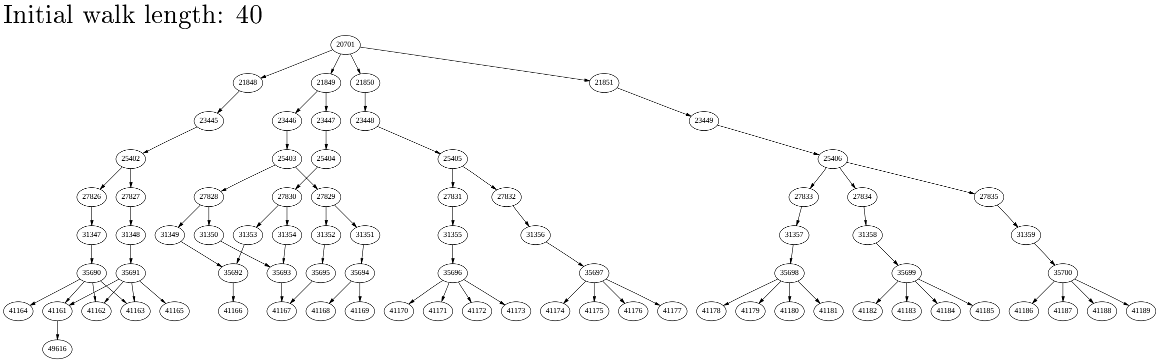
Initial walk length: 0



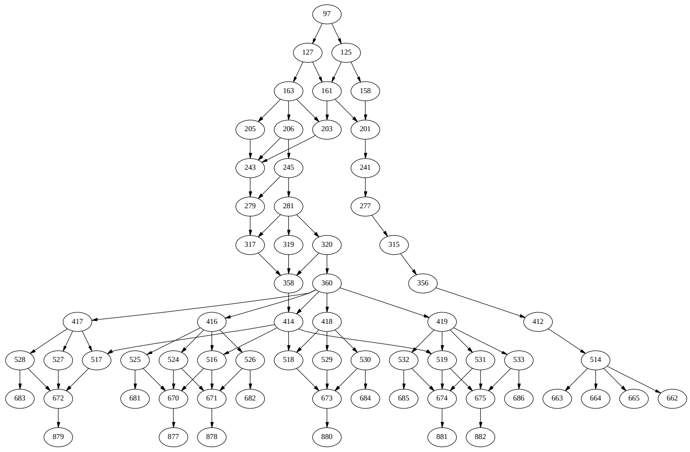
Initial walk length: 20



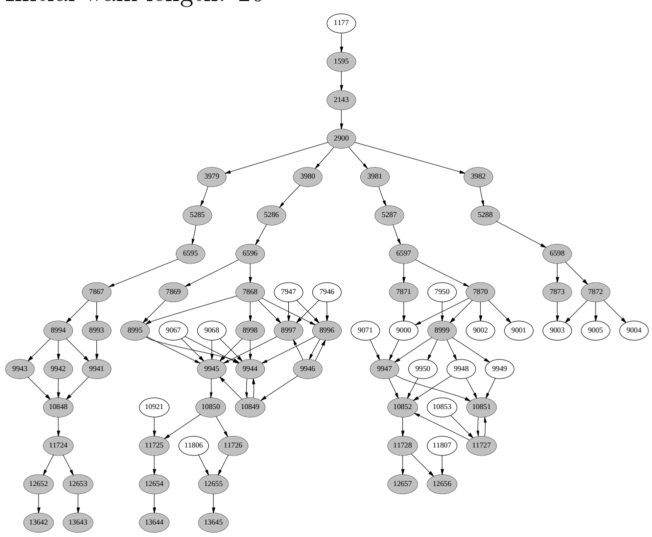
Initial walk length: 40



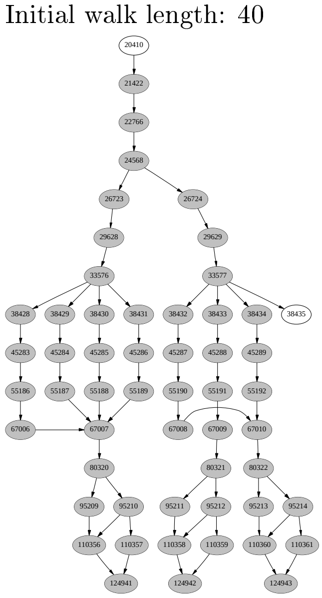
Initial walk length: 60



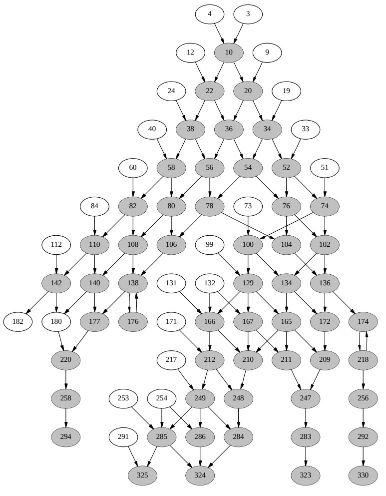
Initial walk length: 20



Initial walk length: 40



Initial walk length: 60



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