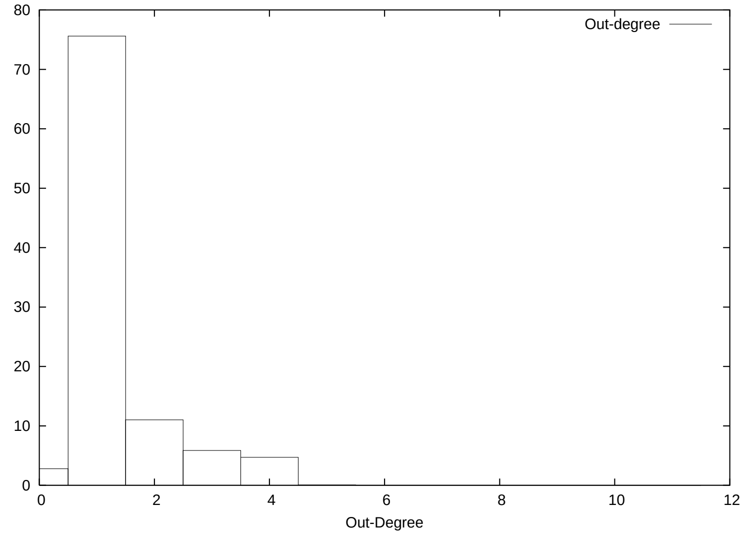
Graph: cadp/scen1_orig_3

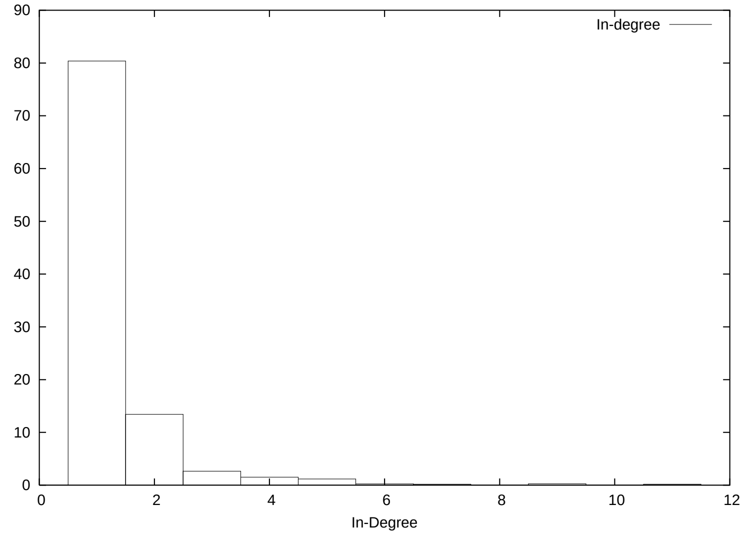
General Info

Example title	IEEE 1394 high performance serial bus
Parameter	3
Reduction	
Modification	version 1, original
States	43,172
Edges	57,948
Average degree	1.3

Out-Degrees distribution:



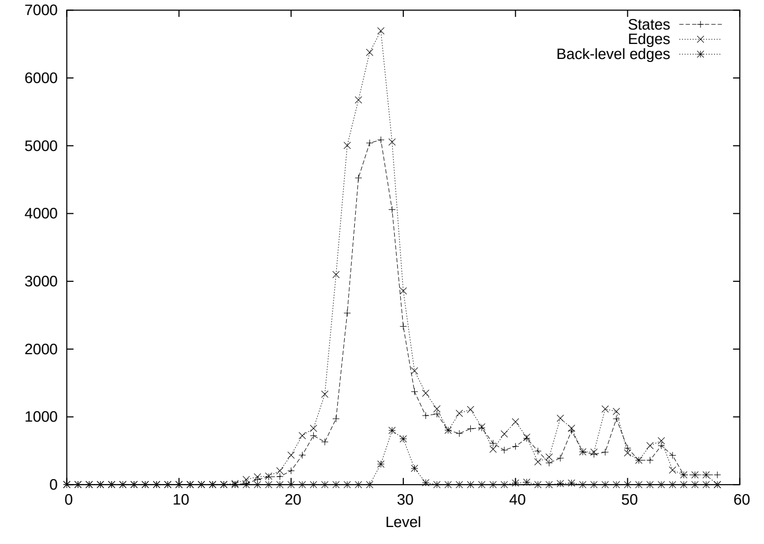
In-Degrees distribution:



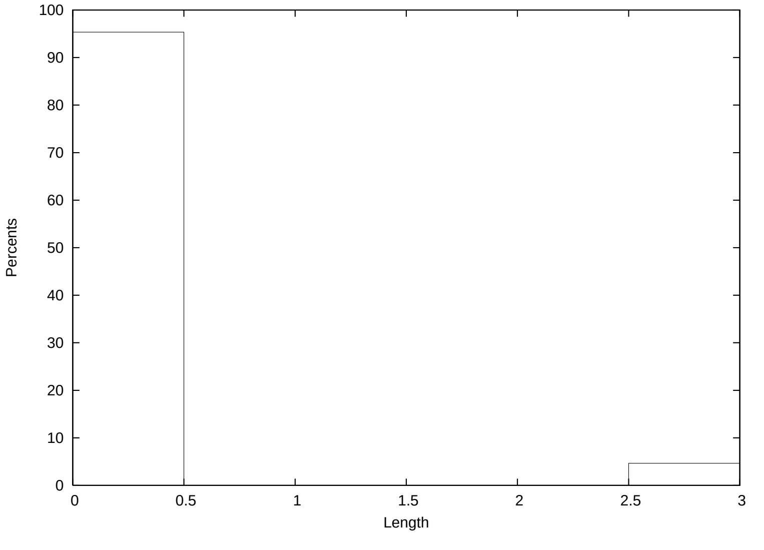
BFS Info

BFS height	58
BFS width	5,087 (11.7%)
Back level edges	2,150 (3.7%)

BFS level info:



Back level edges lengths:



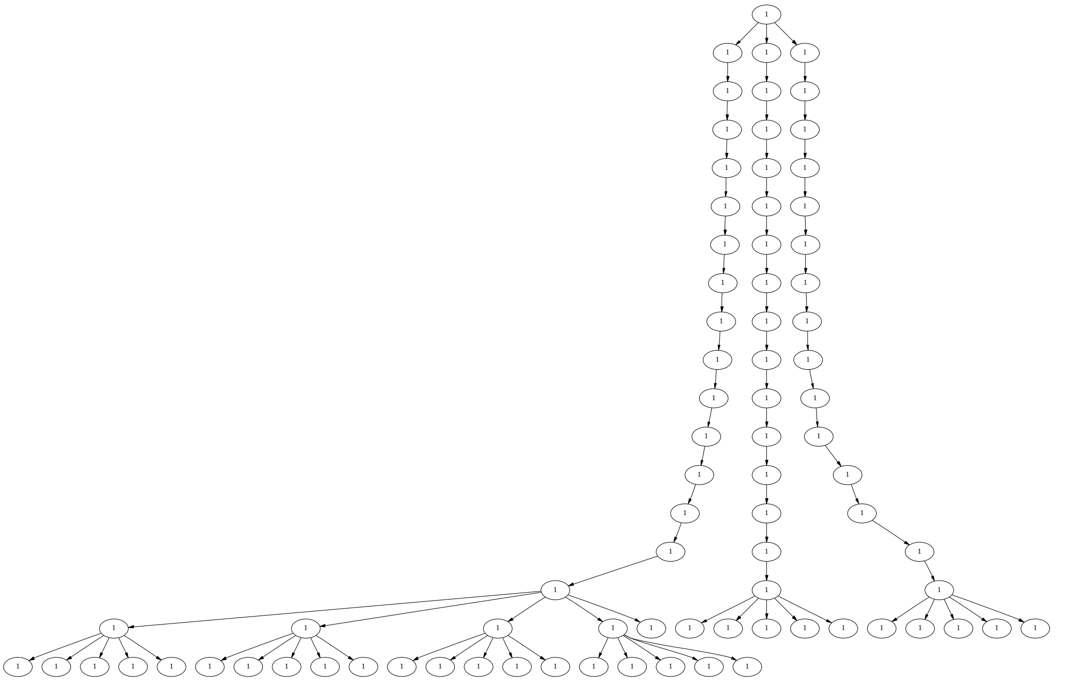
SCC Info

SCC components	43,052
SCC quetient height	62
SCC quetient nontrivial height	2
Number of terminal components	1,204
The largest component is	intermediate

Statistics according to **sizes**:

Size	Number of components	Number of states here
4	40	0.3%
1	43,012	99.6%

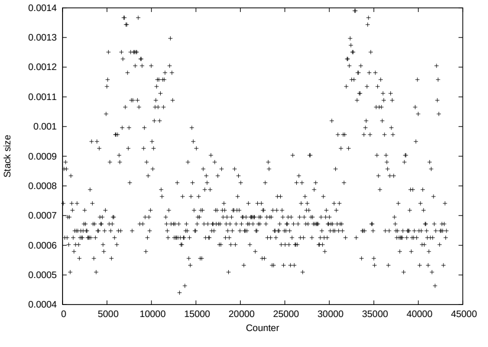
SCC component graph (part):



DFS Info

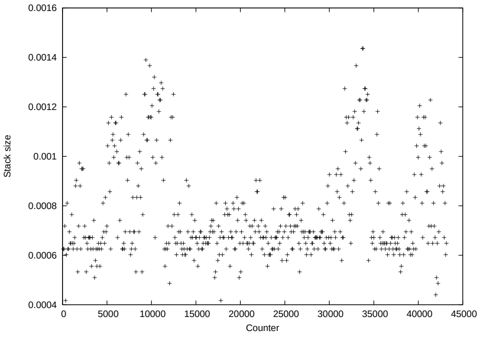
Run 0

front-edges : back-edges : cross-edges = 43,171 : 40 : 14,737 = 74.4%:0%:25.4%
Max. stack size: 63 (0.1%)
Shortest cycle detected: 4
Most often detected lengths of cycles: 4 (40), (), (), (), ()
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 43,171 : 40 : 14,737 = 74.4%:0%:25.4%
Max. stack size: 63 (0.1%)
Shortest cycle detected: 4
Most often detected lengths of cycles: 4 (40), (), (), (), ()
Stack graph:



Local Structure

Short cycles(up to 6)

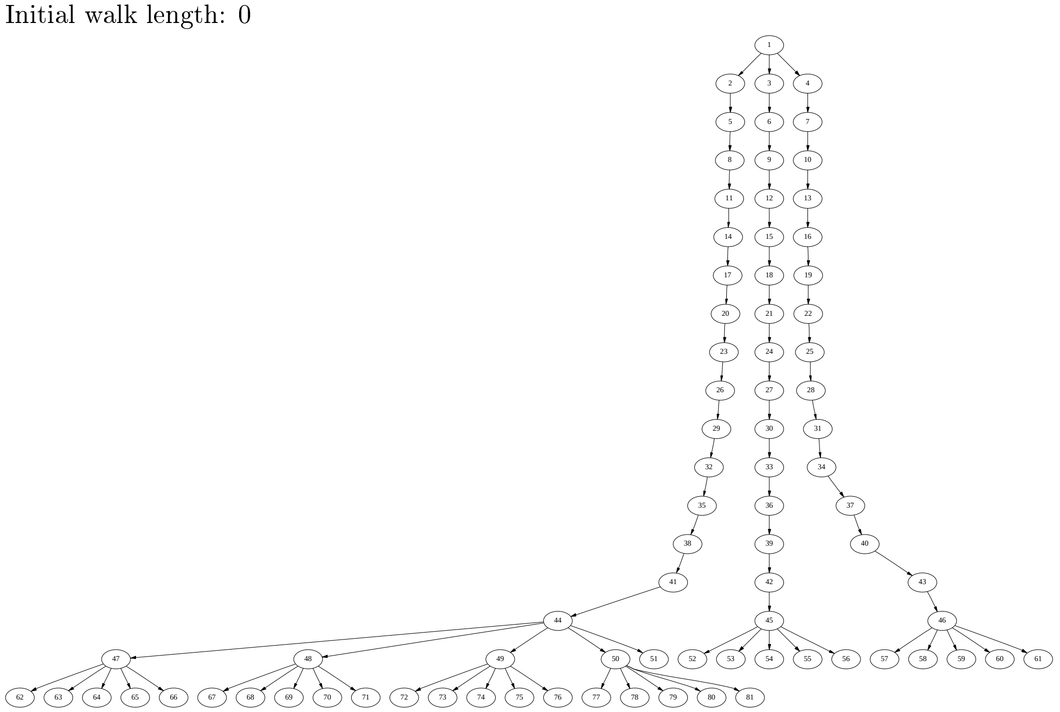
2	0	(0%)
3	0	(0%)
4	160	(0.3%)

Diamonds
5,581 (12.9%)

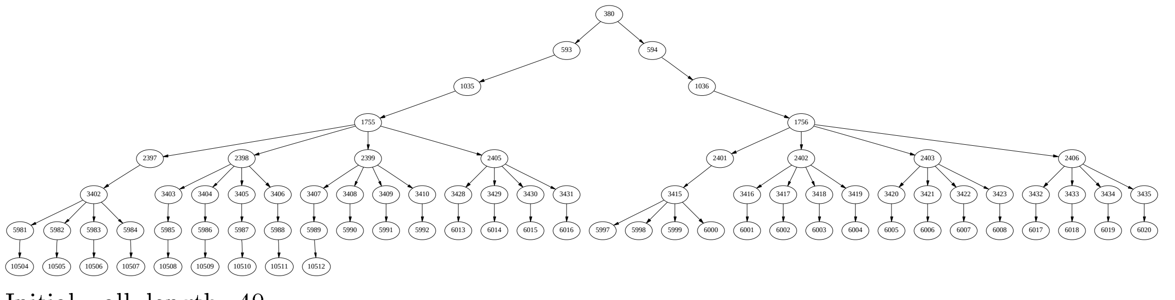
Neighborhoods

k	avg size	avg clustering
1	2.3	1
2	3.8	1
3	5.7	1
4	7.8	1

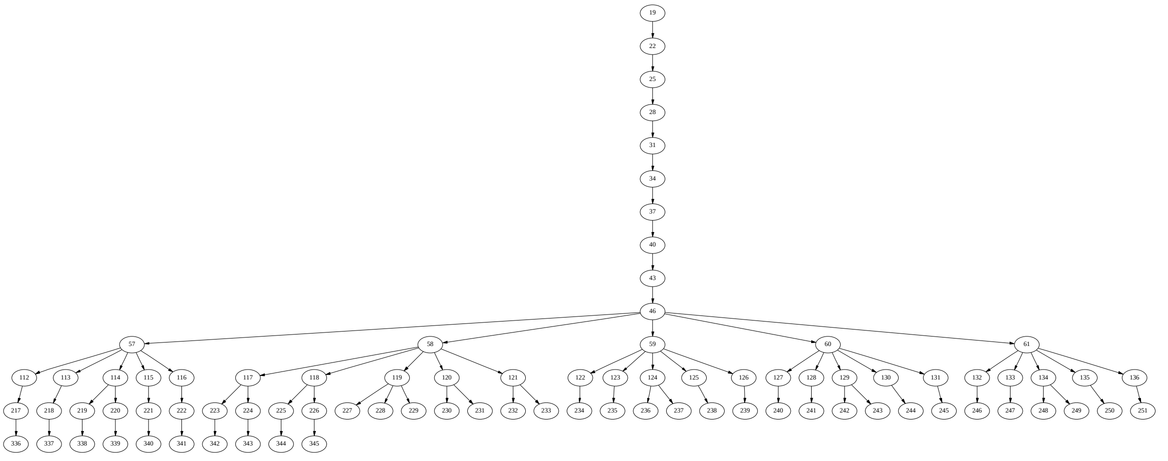
Visualization of some parts of the state state space:
Initial walk length: 0



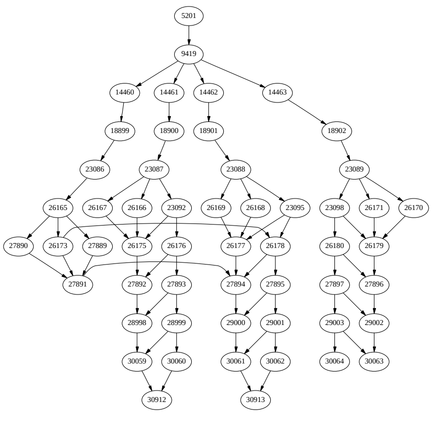
Initial walk length: 20



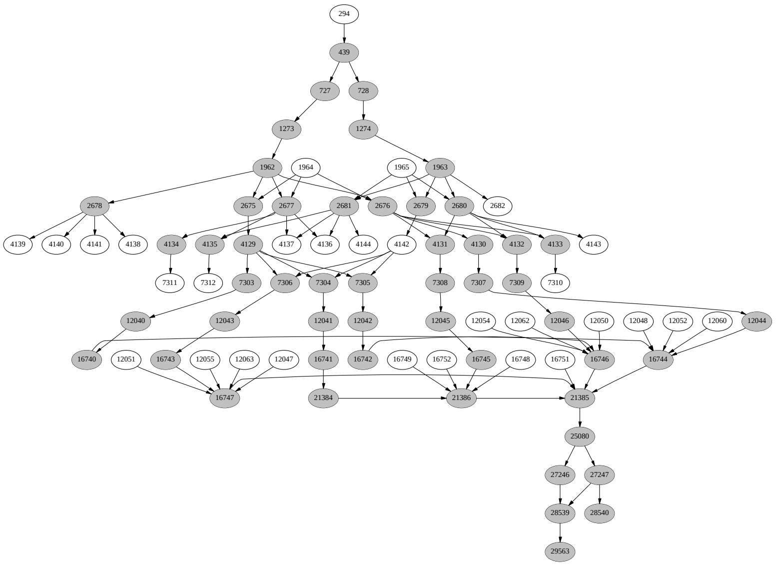
Initial walk length: 40



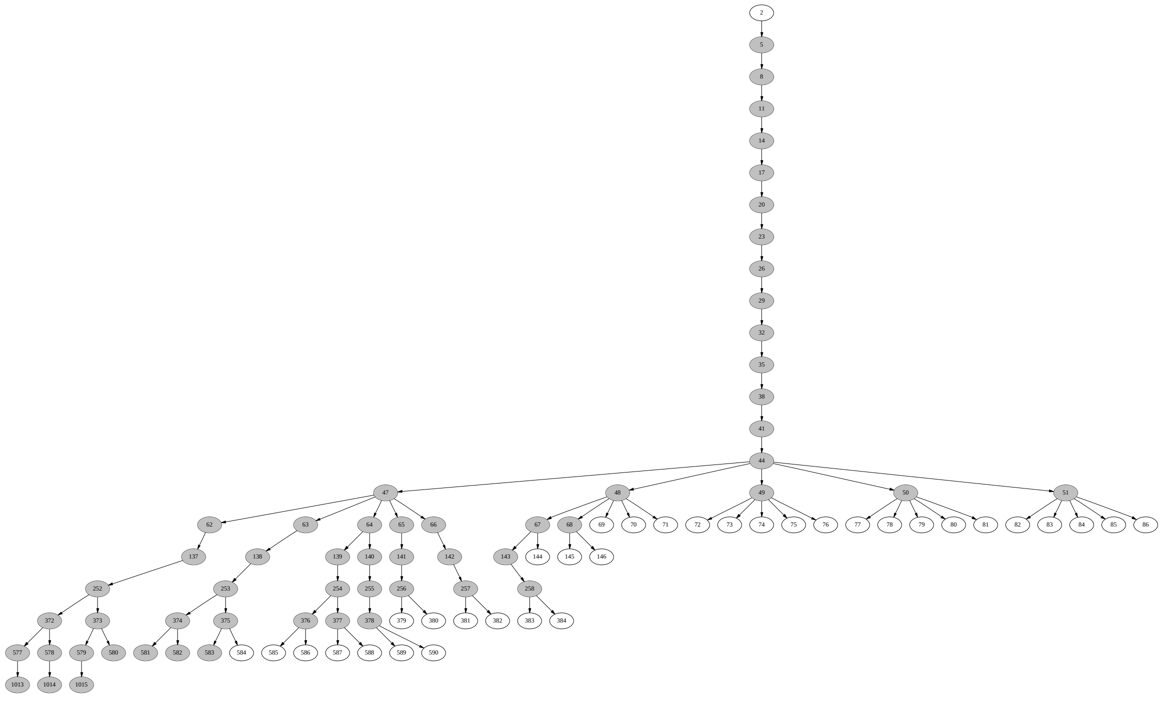
Initial walk length: 60



Initial walk length: 20



Initial walk length: 40



Initial walk length: 60