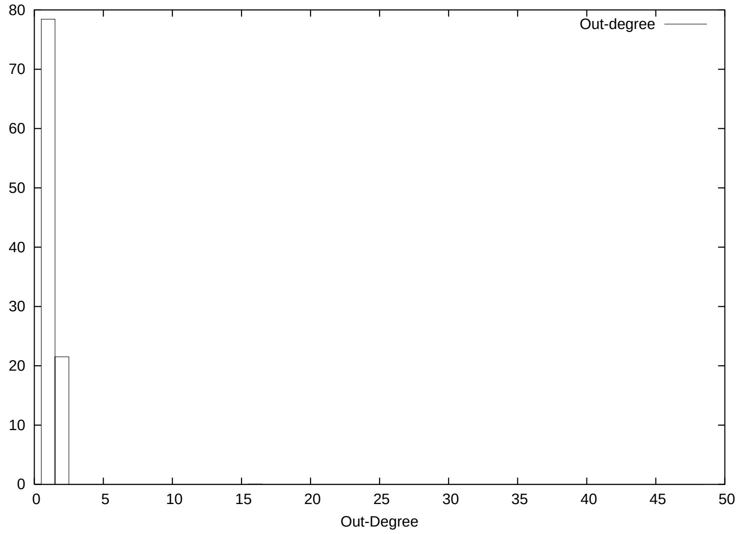
Graph: cadp/cwi_1_2

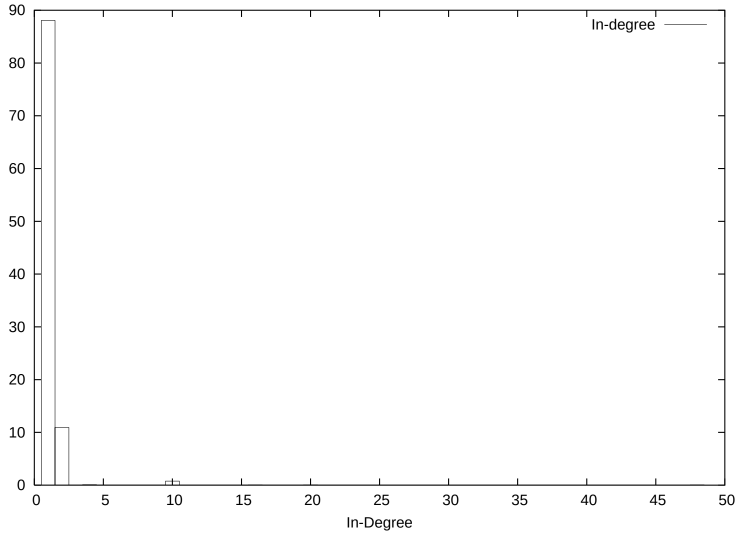
General Info

Example title	VLTS benchmark suite
Parameter	
Reduction	
Modification	
States	1,952
Edges	2,387
Average degree	1.2

Out-Degrees distribution:



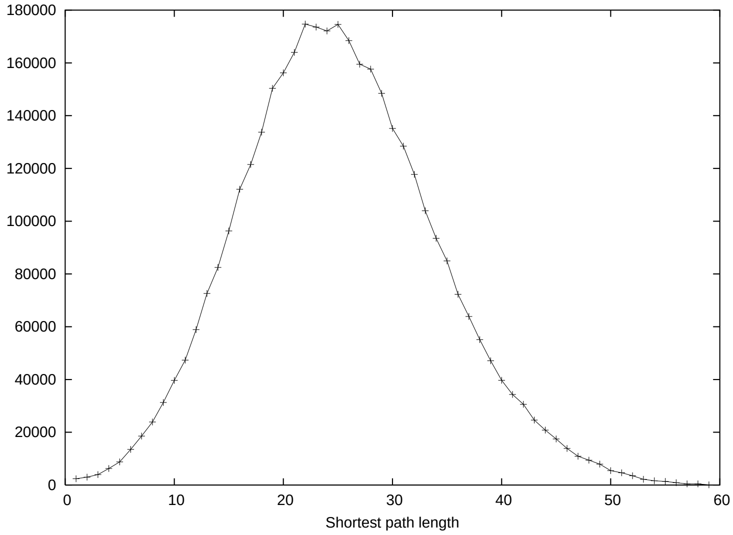
In-Degrees distribution:



Shortest Paths Info

Diameter: 59
Average shortest path length: 25.1453

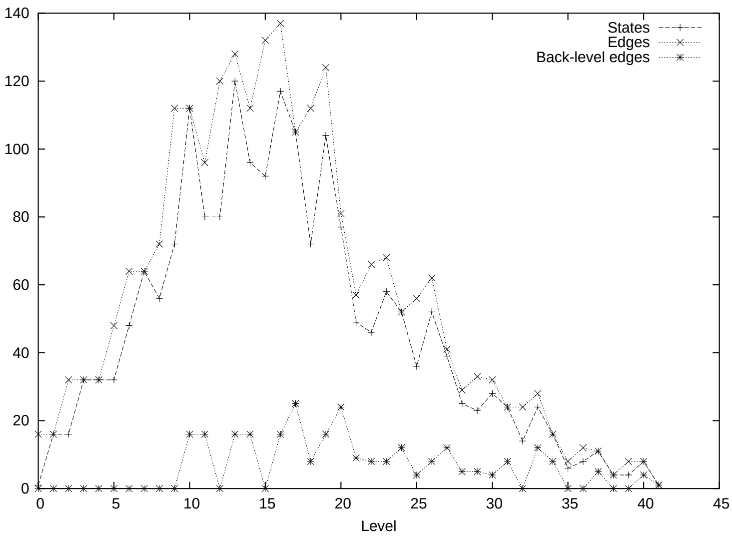
Shortest paths lengths frequencies:



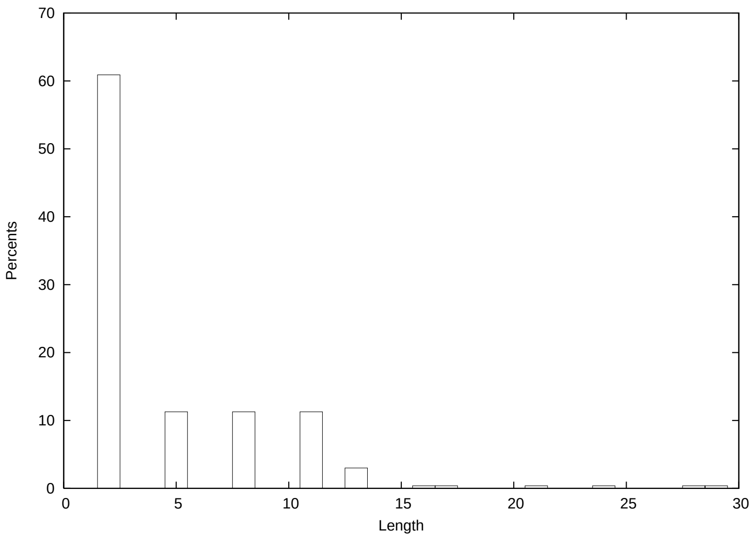
BFS Info

BFS height	41
BFS width	120 (6.1%)
Back level edges	266 (11.1%)

BFS level info:



Back level edges lengths:



SCC Info

SCC components	1
SCC quetient height	0
SCC quetient nontrivial height	0
Number of terminal components	1
The largest component is	initial the only terminal

Statistics according to **sizes**:

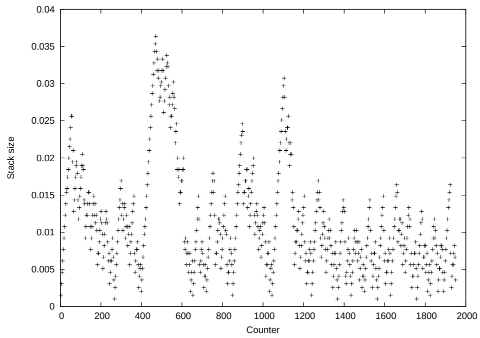
Size	Number of components	Number of states here
1,952	1	100%

SCC component graph (part):

DFS Info

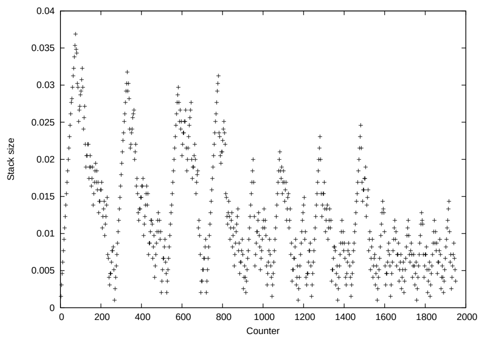
Run 0

front-edges : back-edges : cross-edges = 1,951 : 4 : 432 = 81.7%:0.1%:18%
Max. stack size: 71 (3.6%)
Shortest cycle detected: 30
Most often detected lengths of cycles: 52 (1), 32 (1), 39 (1), 30 (1), ()
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 1,951 : 4 : 432 = 81.7%:0.1%:18%
Max. stack size: 73 (3.7%)
Shortest cycle detected: 44
Most often detected lengths of cycles: 63 (1), 73 (1), 44 (1), 54 (1), ()
Stack graph:



Local Structure

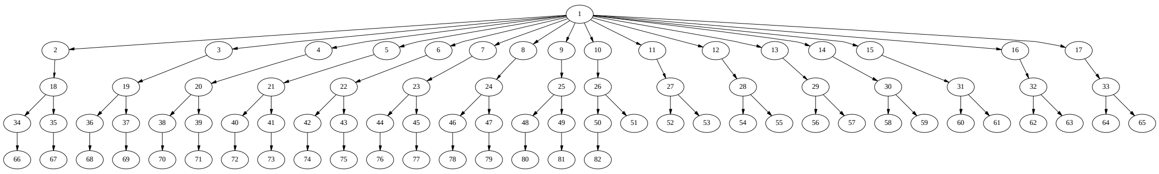
Short cycles(up to 6)
none
Diamonds

0 (0%)

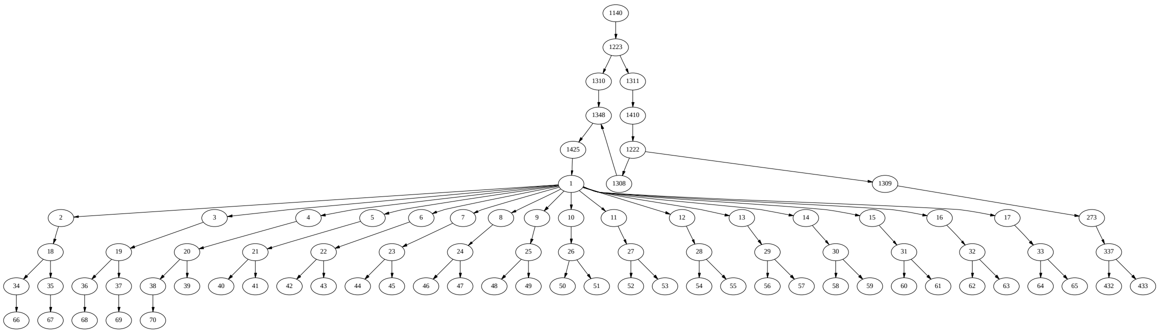
Neighborhoods

k	avg size	avg clustering
1	2.2	1
2	3.7	1
3	5.7	1
4	8.9	1

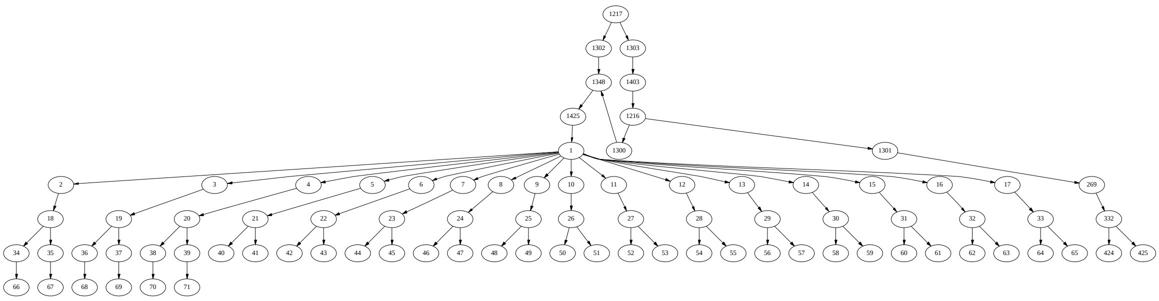
Visualization of some parts of the state state space:
Initial walk length: 0



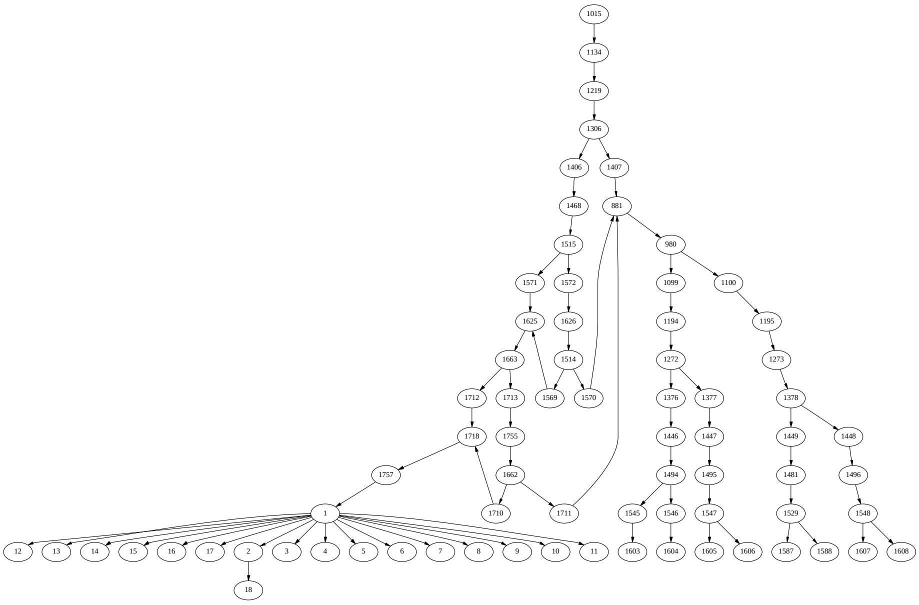
Initial walk length: 20



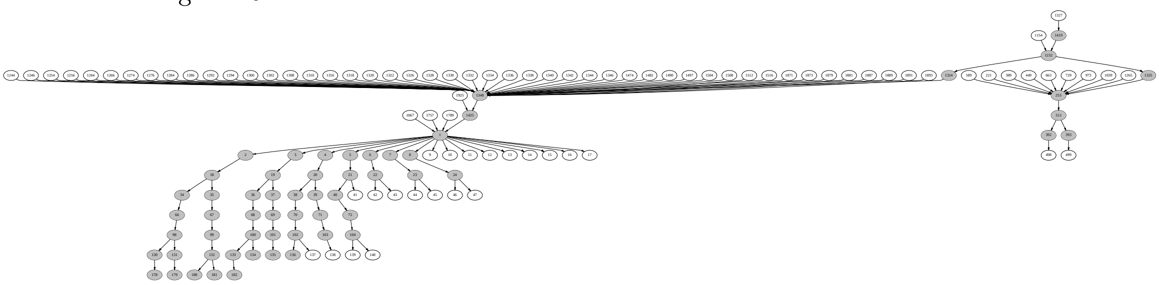
Initial walk length: 40



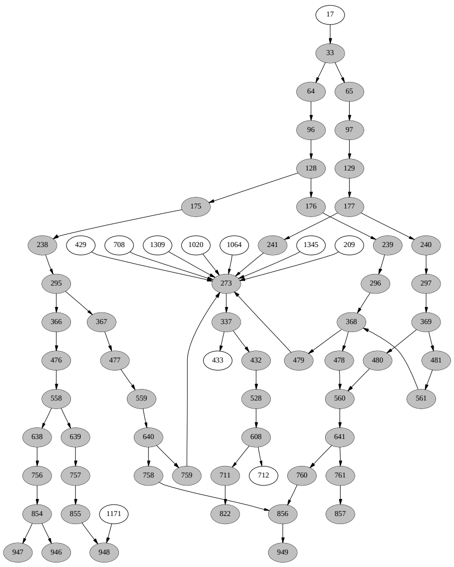
Initial walk length: 60



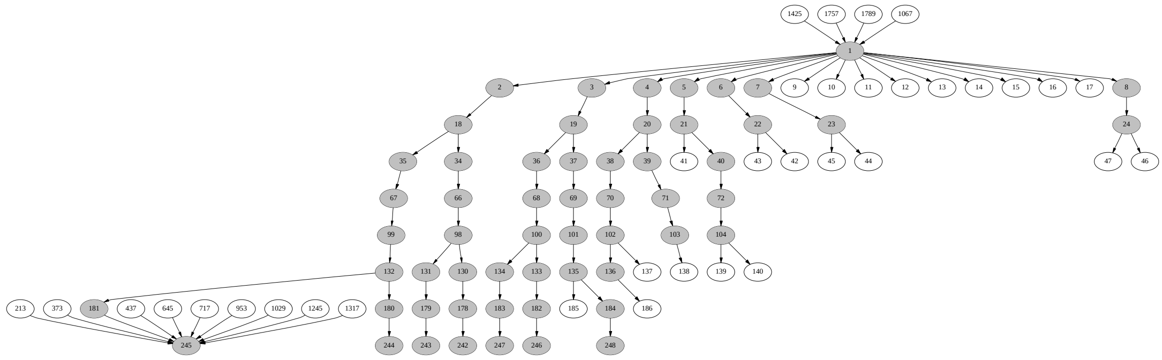
Initial walk length: 20



Initial walk length: 40



Initial walk length: 60


1425
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