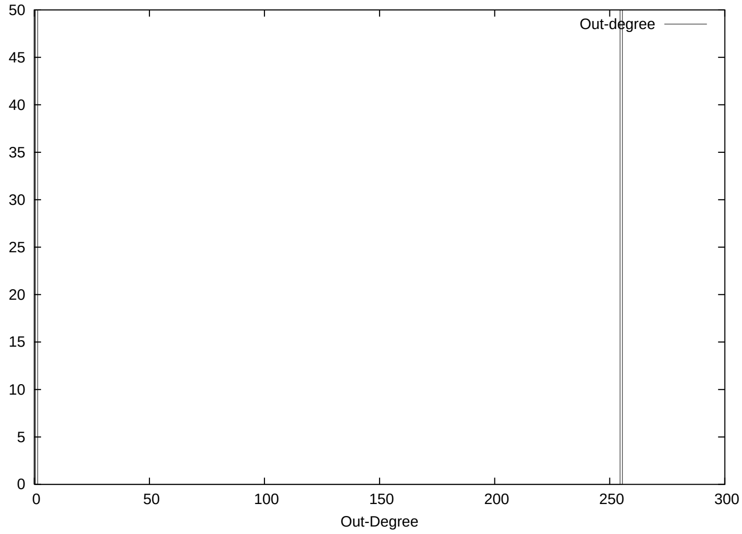
Graph: cadp/bitalt_service

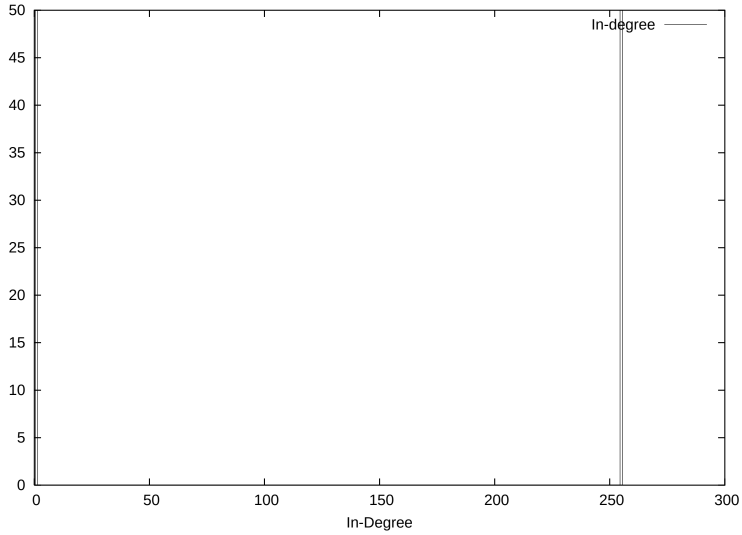
General Info

Example title	Alternating bit
Parameter	
Reduction	
Modification	service
States	510
Edges	65,280
Average degree	128

Out-Degrees distribution:



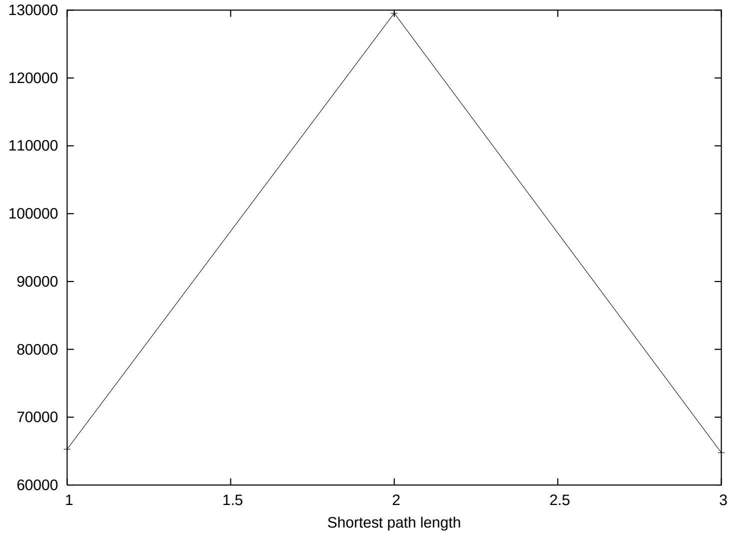
In-Degrees distribution:



Shortest Paths Info

Diameter: 3
Average shortest path length: 1.99804

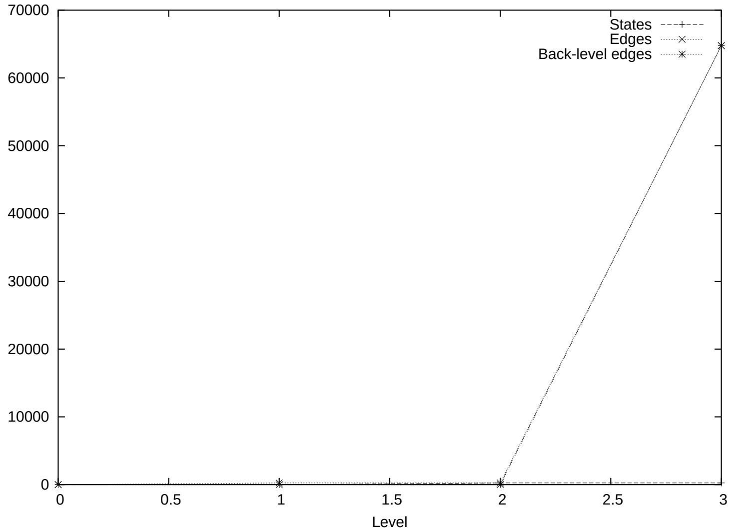
Shortest paths lengths frequencies:



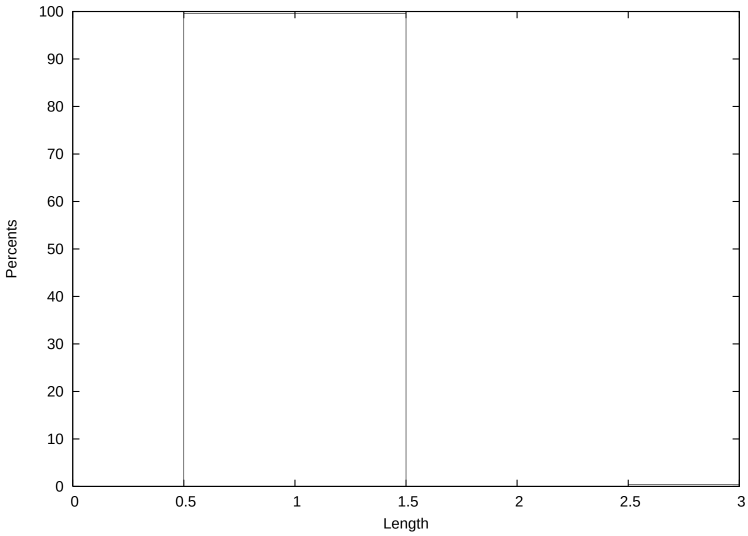
BFS Info

BFS height	3
BFS width	254 (49.8%)
Back level edges	64,771 (99.2%)

BFS level info:



Back level edges lengths:



SCC Info

SCC components	1
SCC quetient height	0
SCC quetient nontrivial height	0
Number of terminal components	1
The largest component is	initial the only terminal

Statistics according to **sizes**:

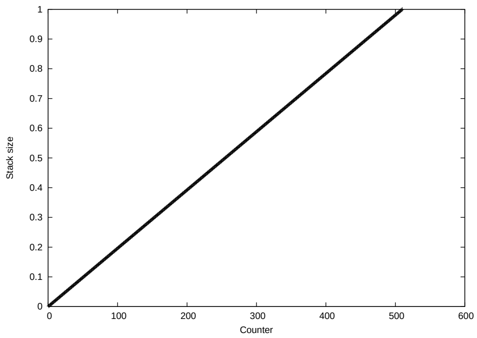
Size	Number of components	Number of states here
510	1	100%

SCC component graph (part):

DFS Info

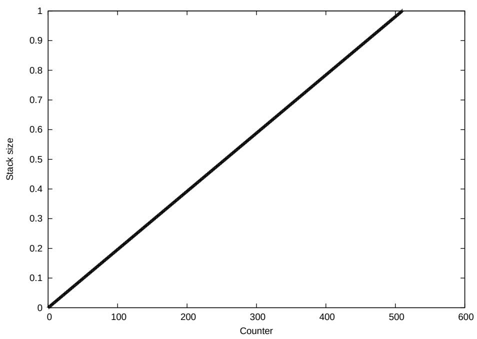
Run 0

front-edges : back-edges : cross-edges = 509 : 32,640 : 32,131 = 0.7%:50%:49.2%
Max. stack size: 510 (100%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 2 (255), 4 (254), 6 (253), 8 (252), 10 (251)
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 509 : 32,640 : 32,131 = 0.7%:50%:49.2%
Max. stack size: 510 (100%)
Shortest cycle detected: 2
Most often detected lengths of cycles: 2 (255), 4 (254), 6 (253), 8 (252), 10 (251)
Stack graph:



Local Structure

Short cycles(up to 6)

2	510	(100%)

Diamonds

0 (0%)

Neighborhoods

k	avg size	avg clustering
1	129	1
2	383	1
3	510	1
4	510	1

Visualization of some parts of the state state space:

Initial walk length: 0

Initial walk length: 20

Initial walk length: 40

Initial walk length: 60

Initial walk length: 20

Initial walk length: 40

Initial walk length: 60