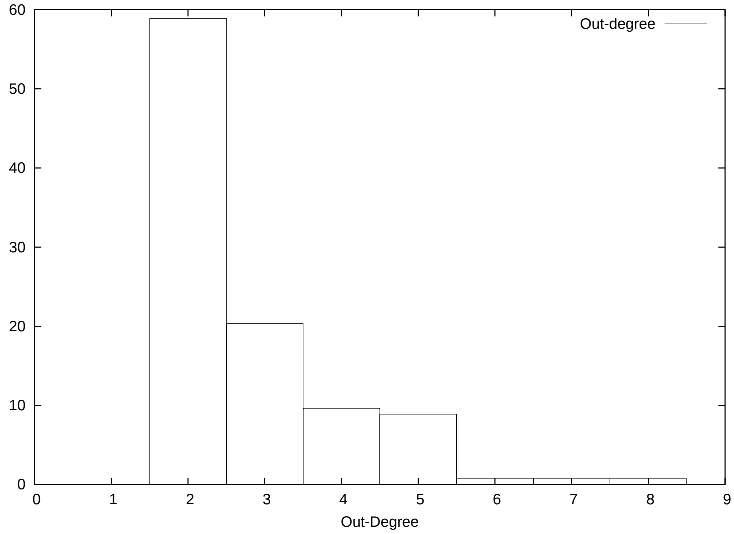
Graph: cadp/bitalt

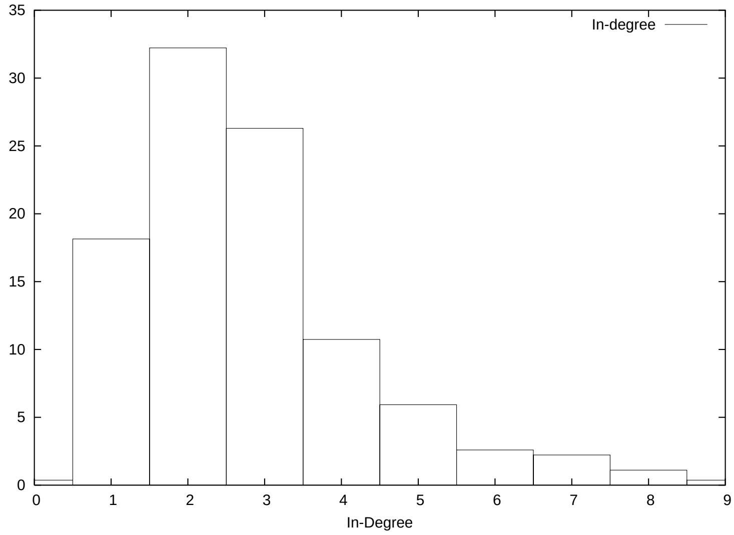
General Info

Example title	Alternating bit
Parameter	
Reduction	
Modification	
States	270
Edges	749
Average degree	2.7

Out-Degrees distribution:



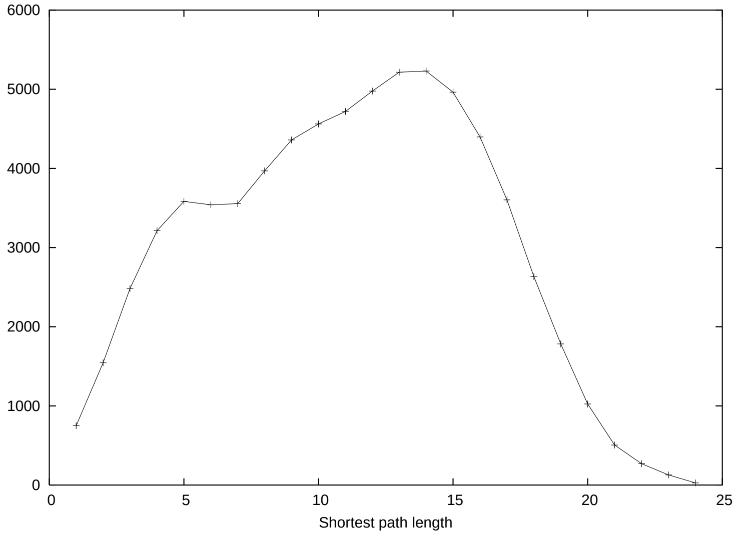
In-Degrees distribution:



Shortest Paths Info

Diameter: 24
Average shortest path length: 11.0567

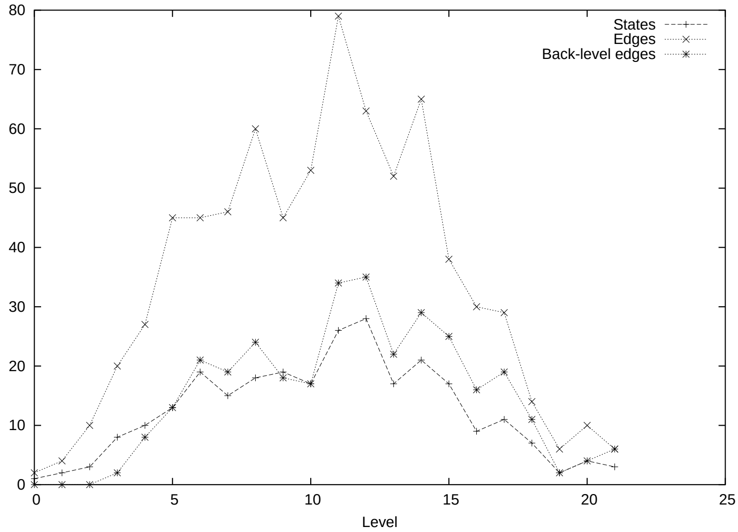
Shortest paths lengths frequencies:



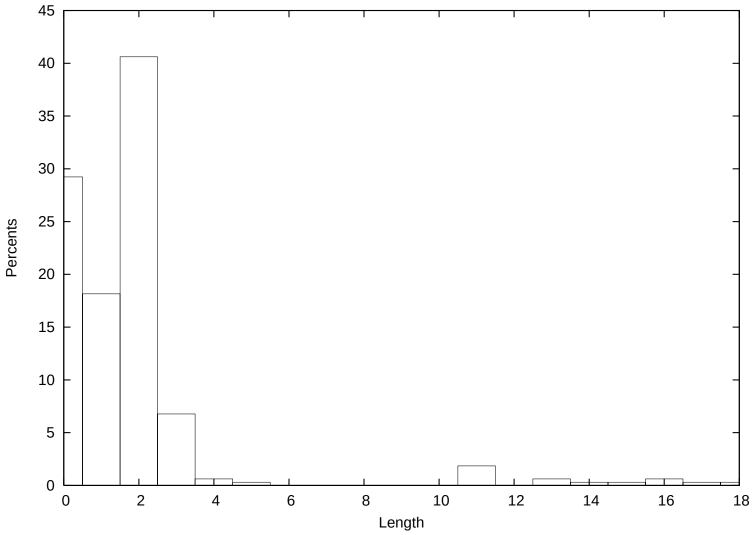
BFS Info

BFS height	21
BFS width	28 (10.3%)
Back level edges	325 (43.3%)

BFS level info:



Back level edges lengths:



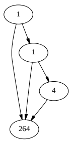
SCC Info

SCC components	4
SCC quetient height	3
SCC quetient nontrivial height	2
Number of terminal components	1
The largest component is	the only terminal

Statistics according to **sizes**:

Size	Number of components	Number of states here
264	1	97.7%
4	1	1.4%
1	2	0.7%

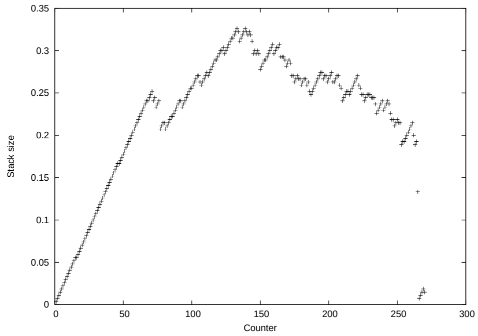
SCC component graph (part):



DFS Info

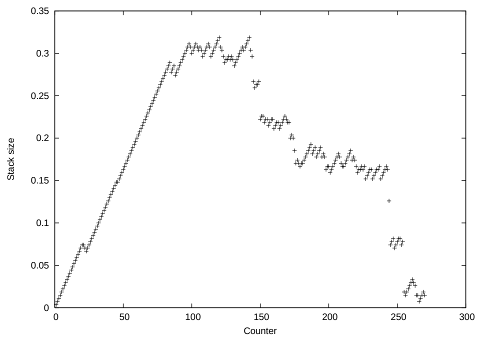
Run 0

front-edges : back-edges : cross-edges = 269 : 235 : 245 = 35.9%:31.3%:32.7%
Max. stack size: 88 (32.5%)
Shortest cycle detected: 3
Most often detected lengths of cycles: 3 (85), 6 (15), 36 (13), 5 (13), 40 (8)
Stack graph:



Run 1

front-edges : back-edges : cross-edges = 269 : 187 : 293 = 35.9%:24.9%:39.1%
Max. stack size: 86 (31.8%)
Shortest cycle detected: 3
Most often detected lengths of cycles: 3 (102), 6 (14), 5 (12), 14 (8), 11 (8)
Stack graph:



Local Structure

Short cycles(up to 6)

2	0	(0%)
3	244	(90.3%)

Diamonds

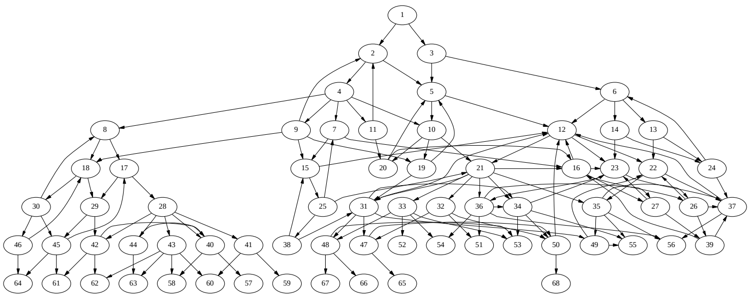
264 (97.7%)

Neighborhoods

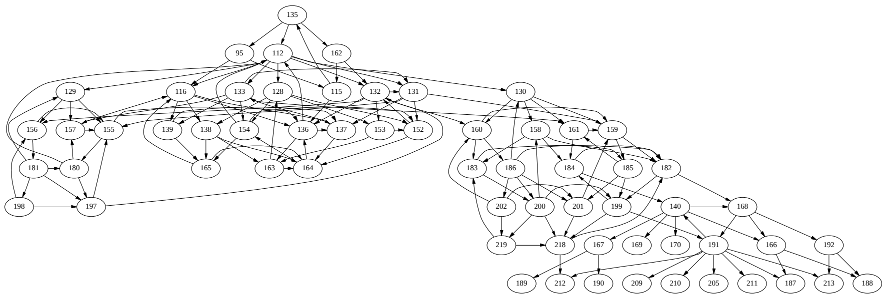
k	avg size	avg clustering
1	3.7	1
2	9.4	1.2
3	18.6	1.6
4	30.5	1.9

Visualization of some parts of the state state space:

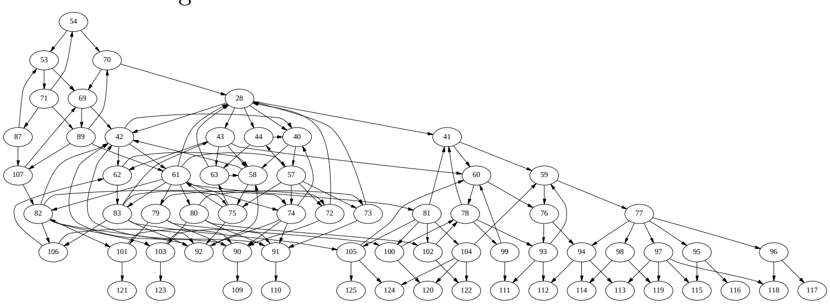
Initial walk length: 0



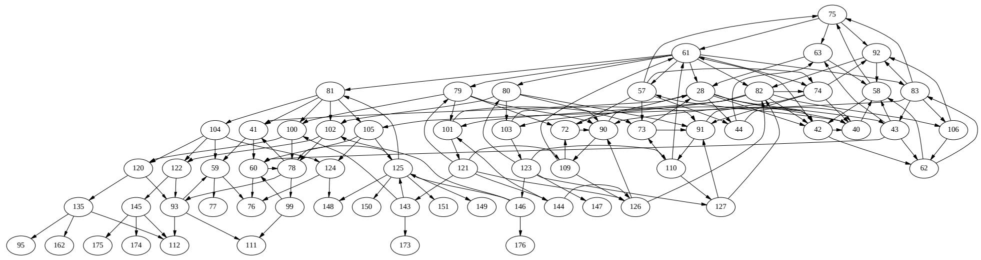
Initial walk length: 20



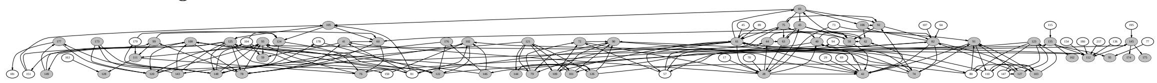
Initial walk length: 40



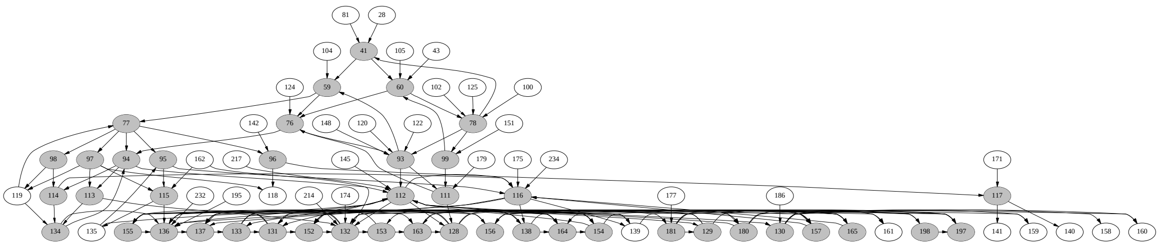
Initial walk length: 60



Initial walk length: 20



Initial walk length: 40



Initial walk length: 60